

DC/DC POWER

+1.5V POWER

+1.8V POWER

+2.5V POWER

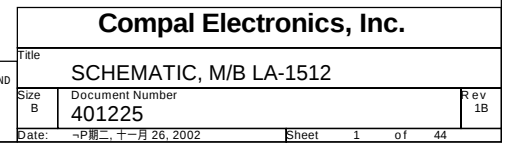
+3VALW POWER

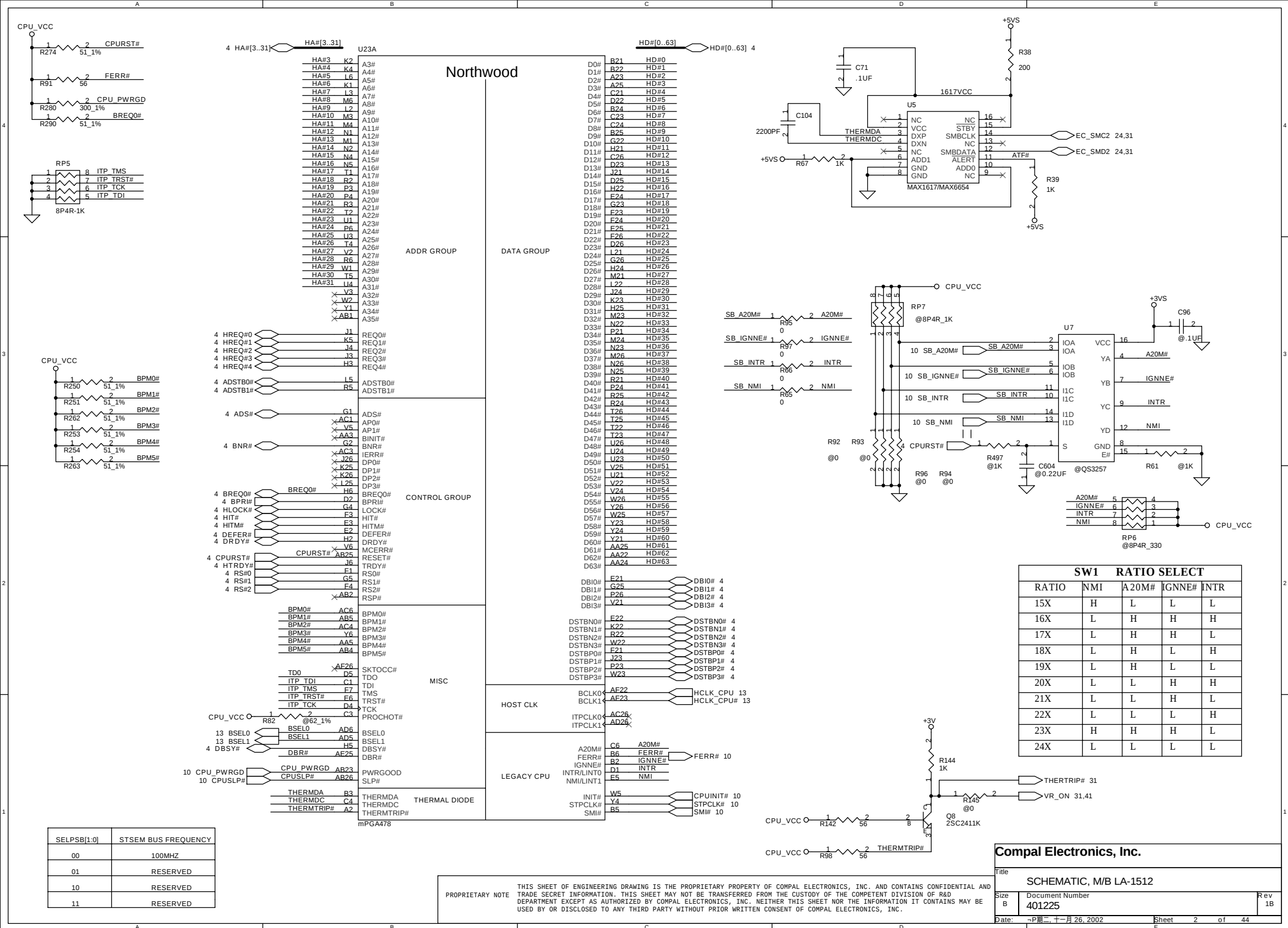
+5VALW POWER

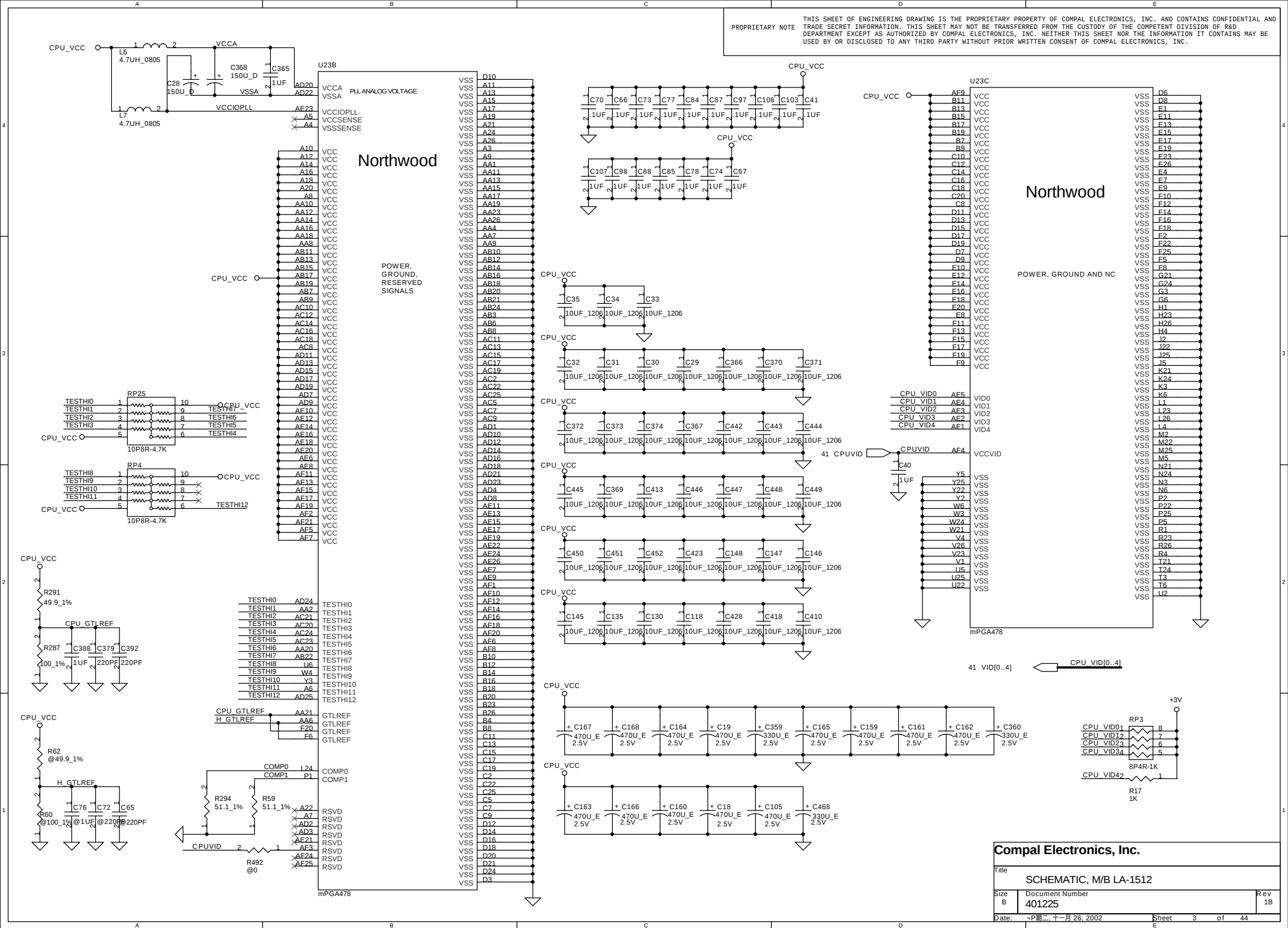
+12VALW POWER

CPU_VCC POWER

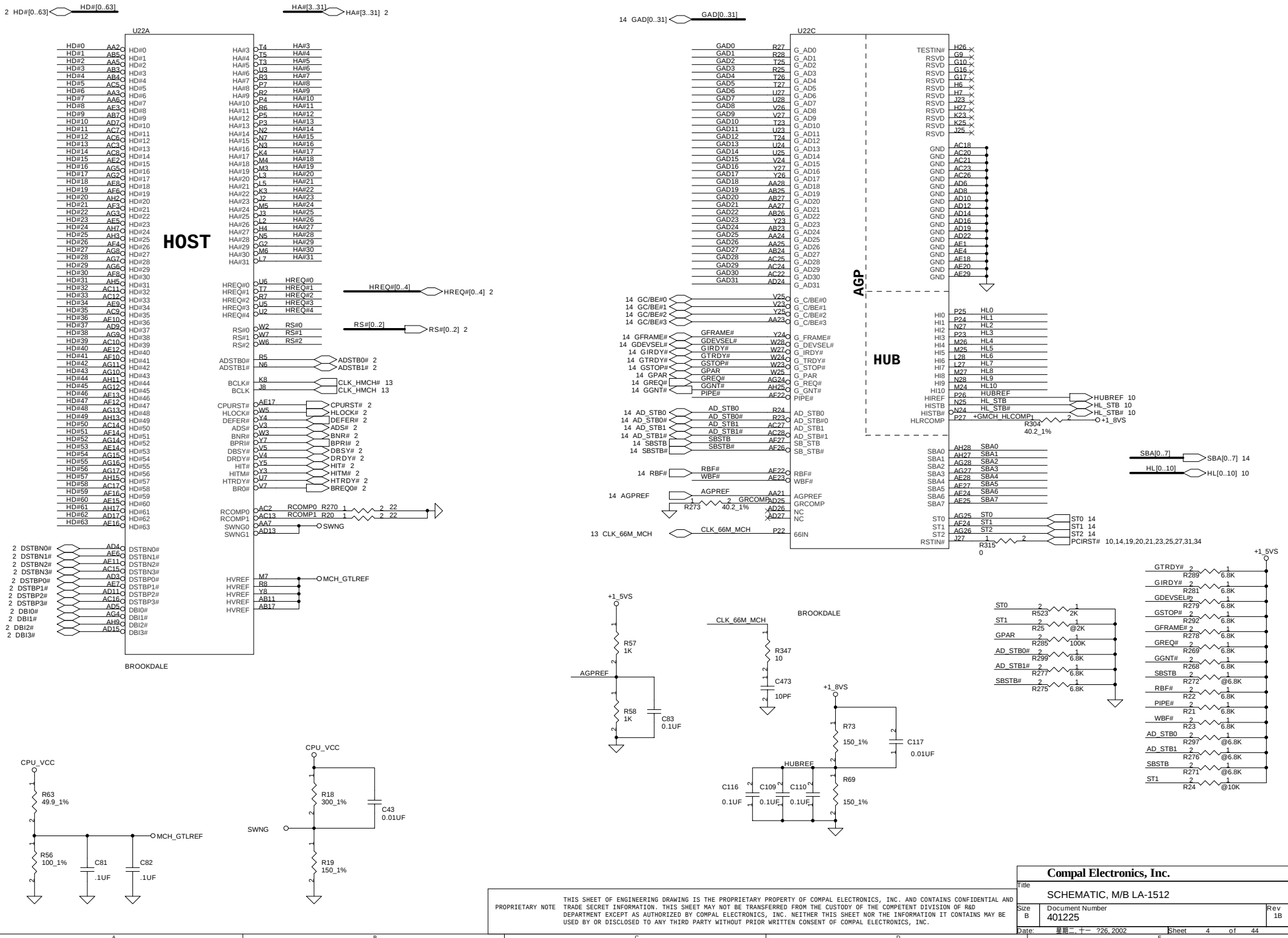
PAGE 36,37,38,39,40,41





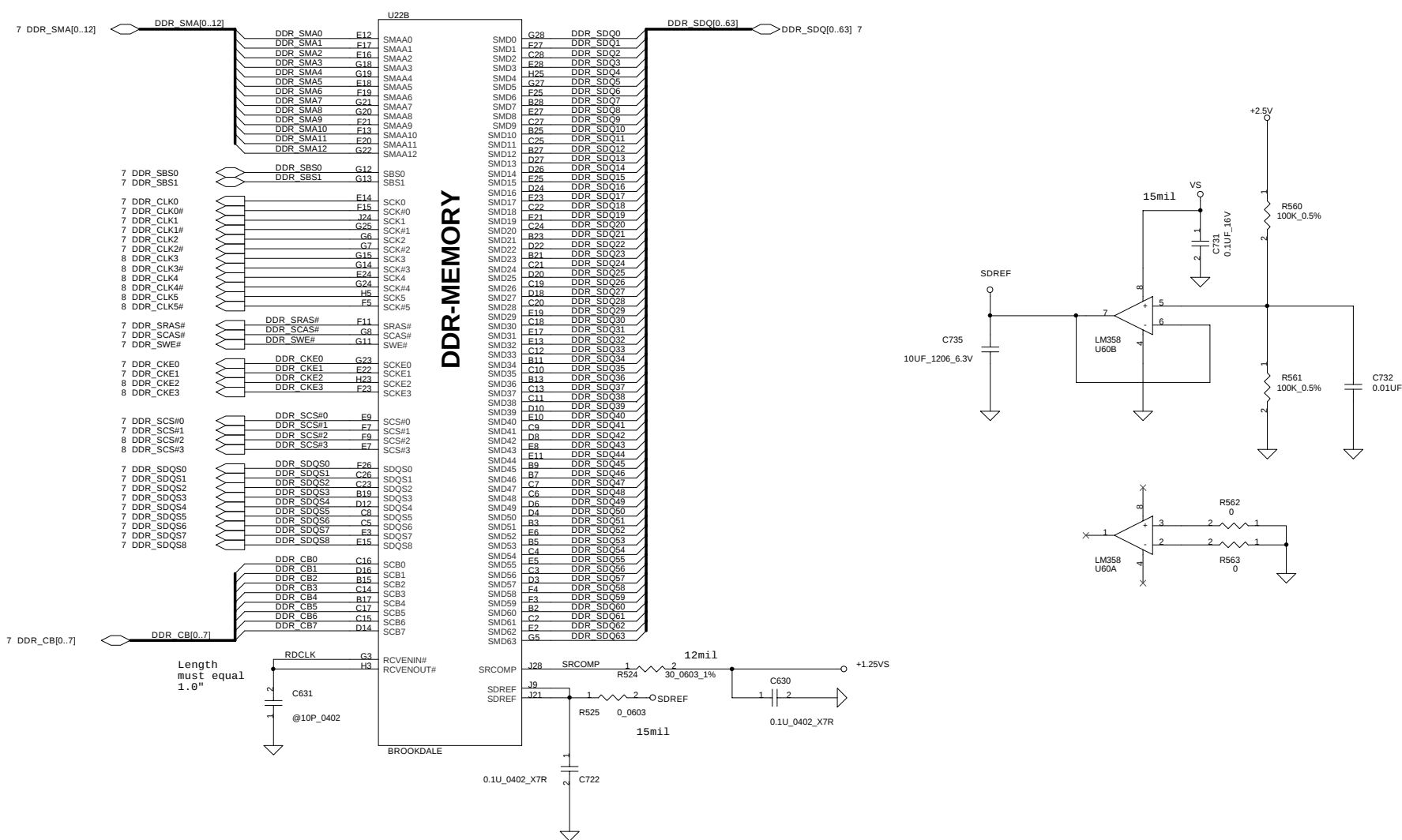


MC-1/3(GTL+,AGP,HUB)

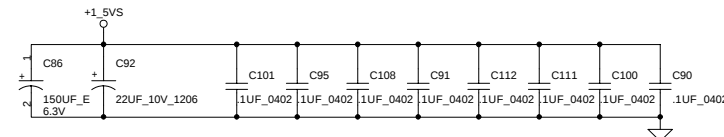
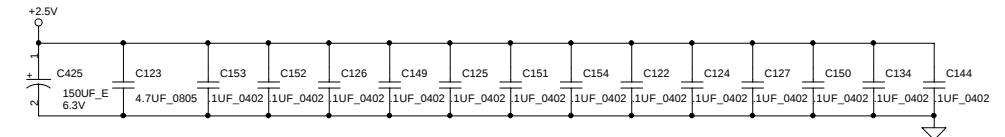
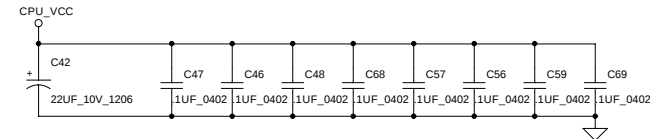
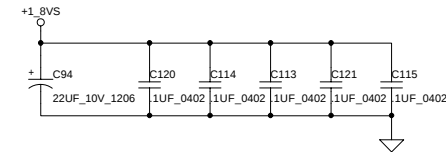
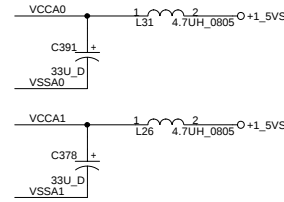
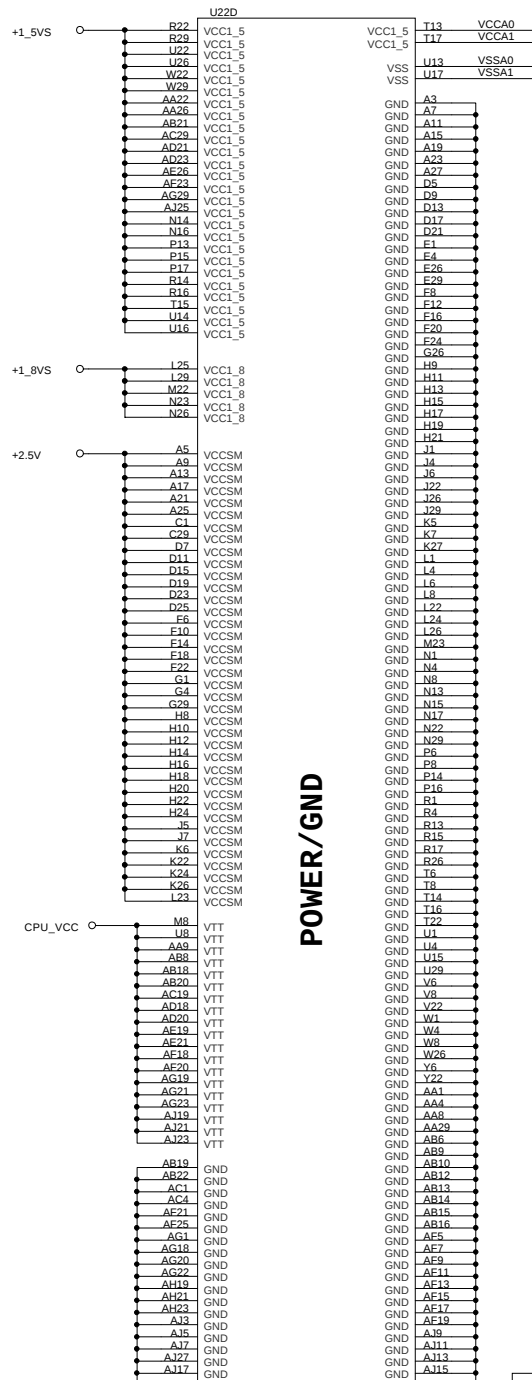


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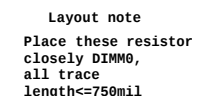
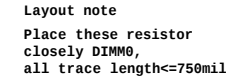
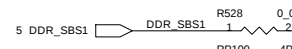
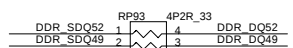
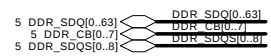


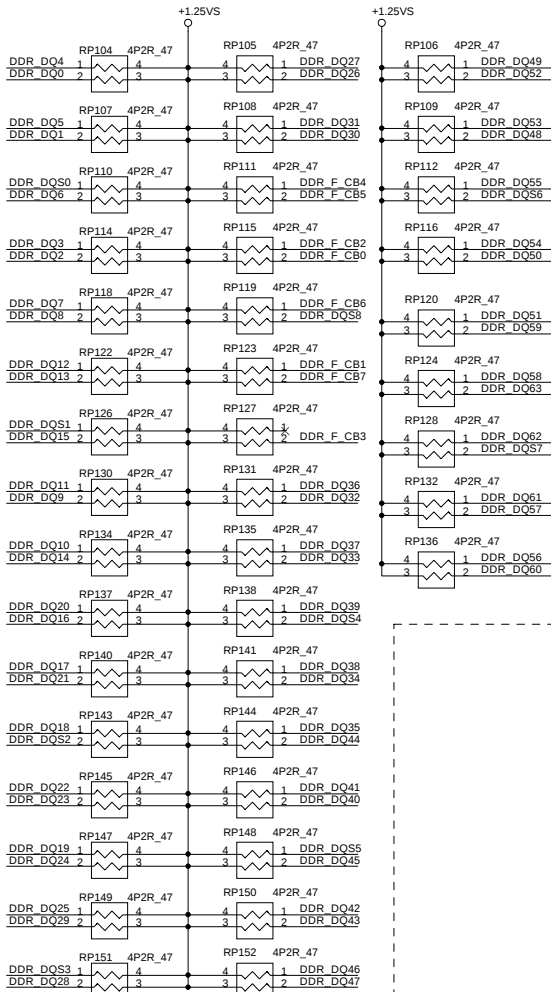
MCH-3/3(Power)



POWER/GND

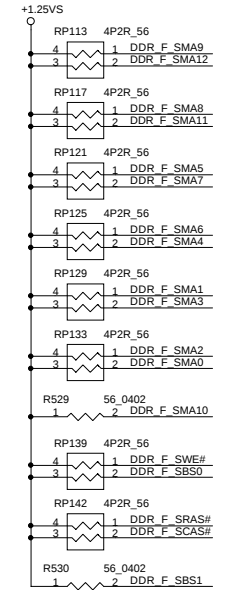
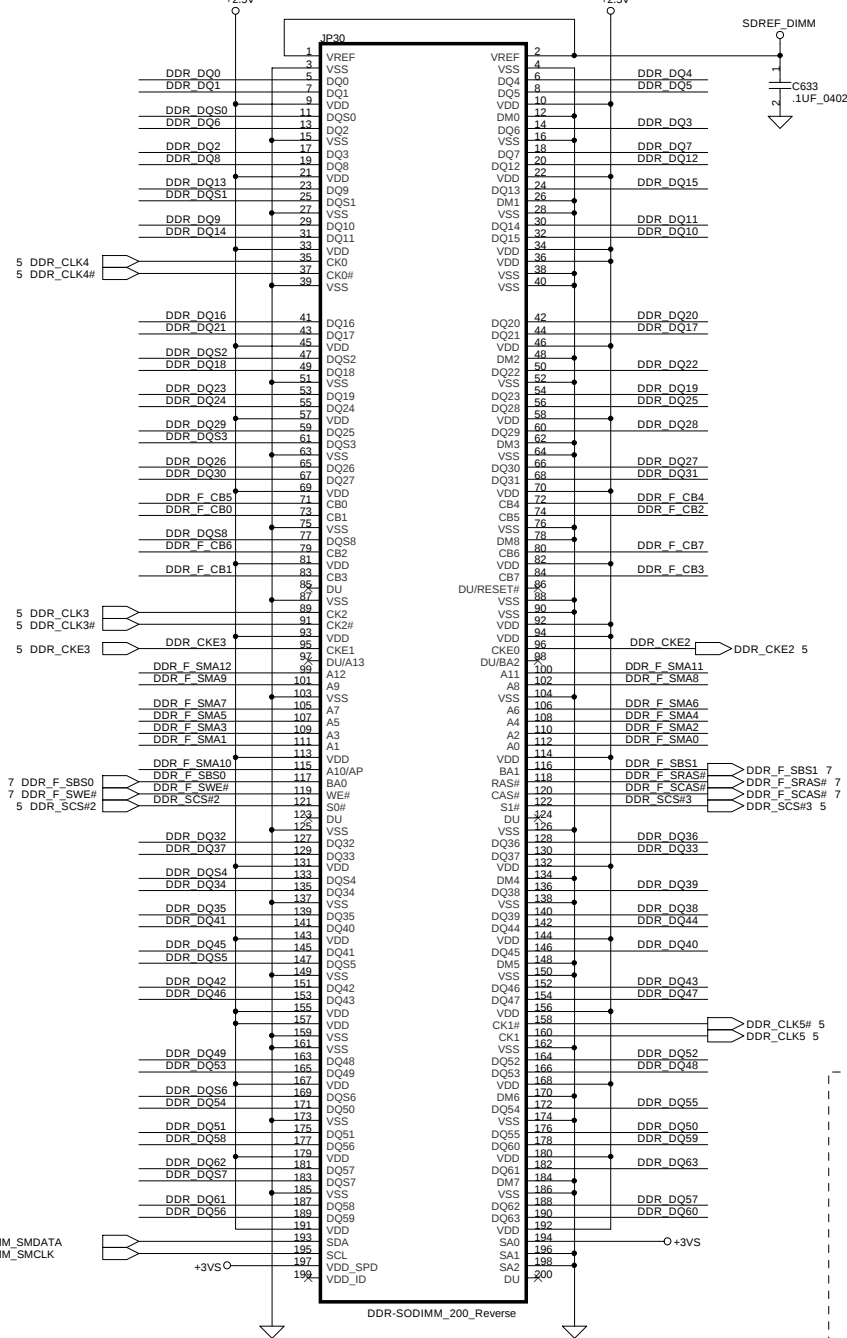
Place these resistor
closely DIMM0,
all trace length<750mil





Layout note
Place these resistor
closely DIMM1,
all trace
length<=800mil

DDR_F_CB[0..7] 7
DDR_DQS[0..8] 7
DDR_DQ[0..63] 7
DDR_F_SMA[0..12] 7



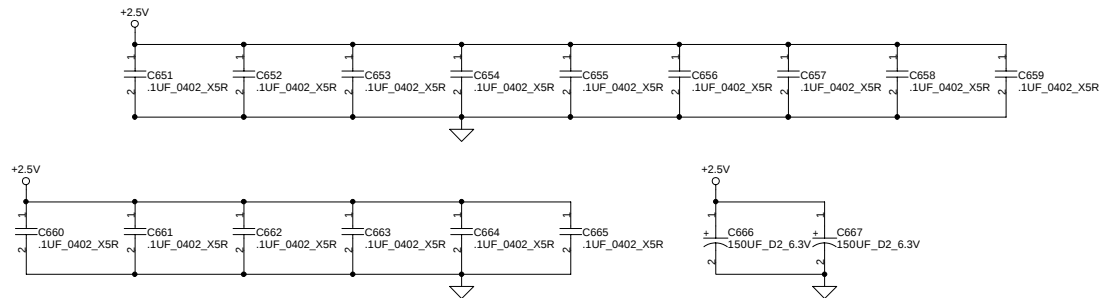
Layout note
Place these resistor
closely DIMM1,
all trace length
Max=1.3"

DIMM1 (REV)
Bottom side

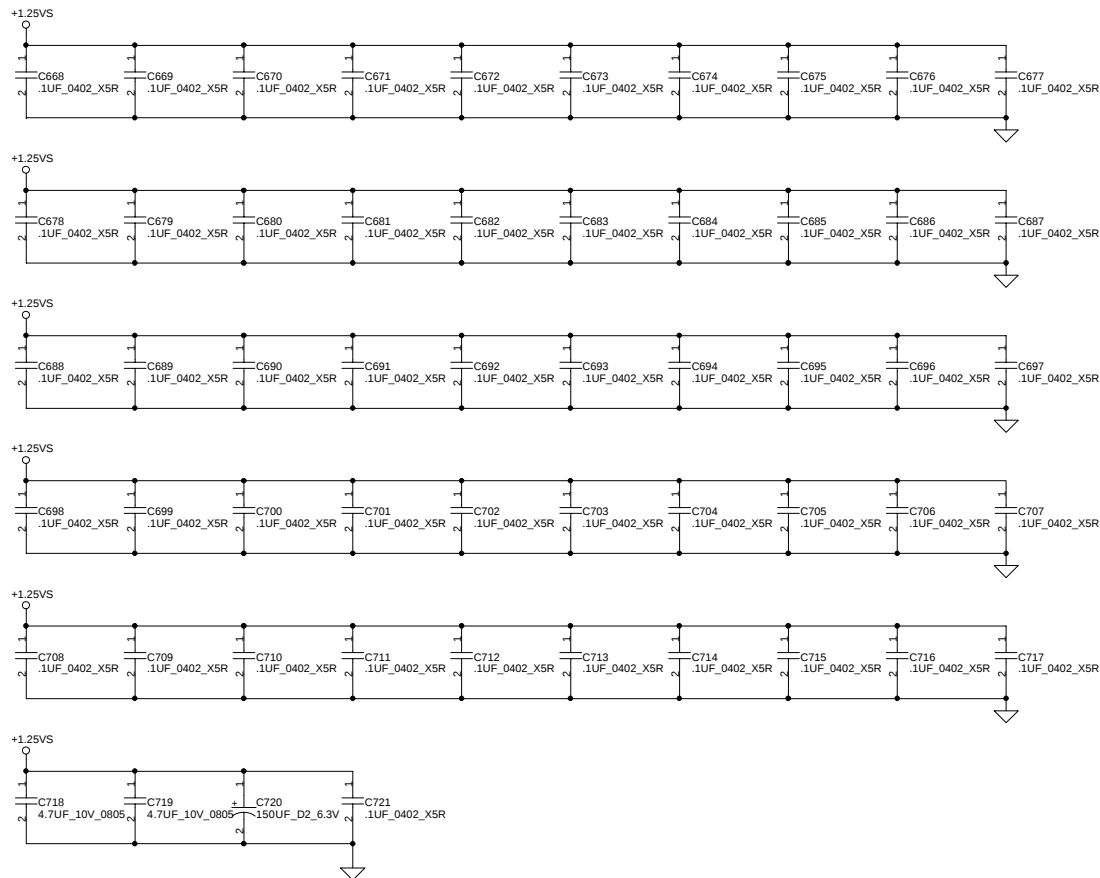
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Layout note :
Distribute as close as possible
to DDR-SODIMM.



Layout note :
Place one cap close to every 2 pull up resistors termination to
+1.25V



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(FW82801BA)

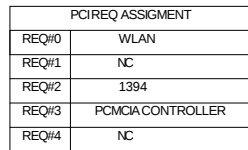
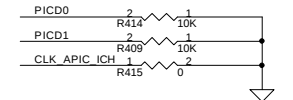
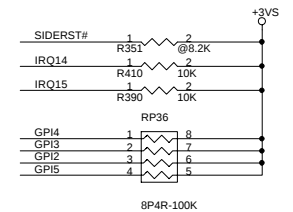
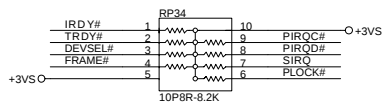


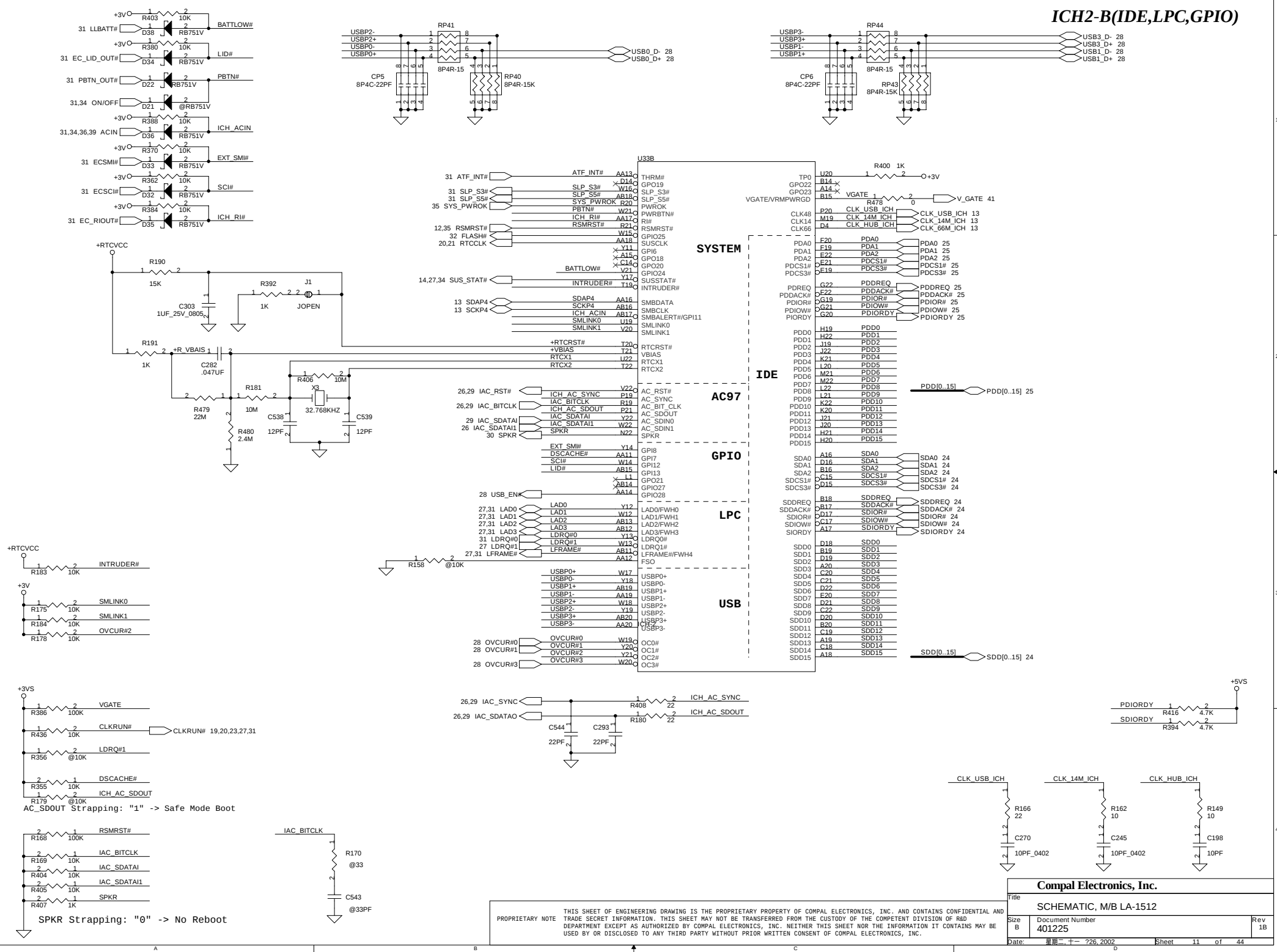
Figure 1-10 shows the RP35 pin connections. The RP35 component has pins 1 through 10. The connections are as follows:

- PIN 1: PERR#
- PIN 2: REQA#
- PIN 3: STOP#
- PIN 4: SERR#
- PIN 5: +3VS
- PIN 6: REQ#4
- PIN 7: REQ#4
- PIN 8: PIROB#
- PIN 9: PIRQA#
- PIN 10: +3VS

A 10P8R-8.2K component is connected to pins 6 and 7.

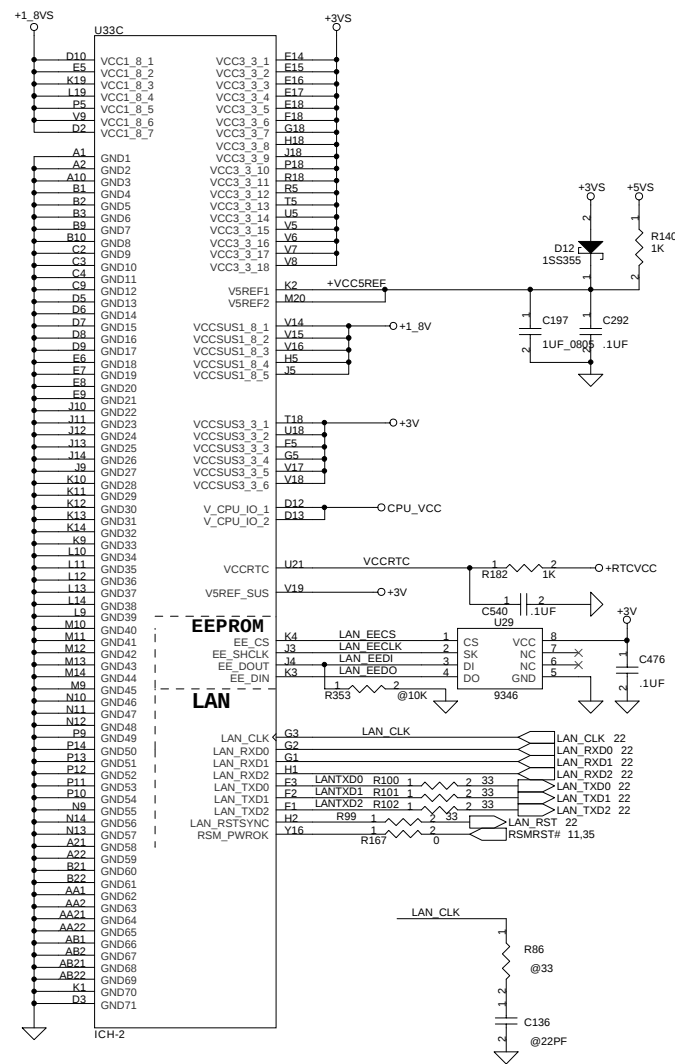
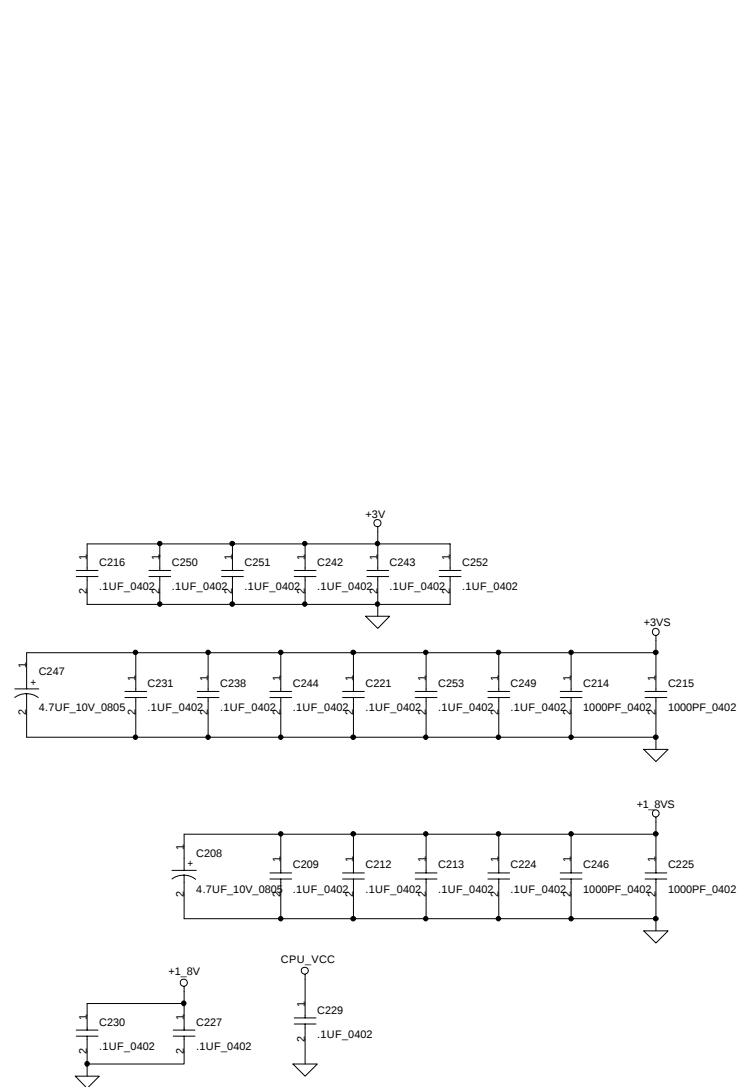


ICH2-B(IDE,LPC,GPIO)



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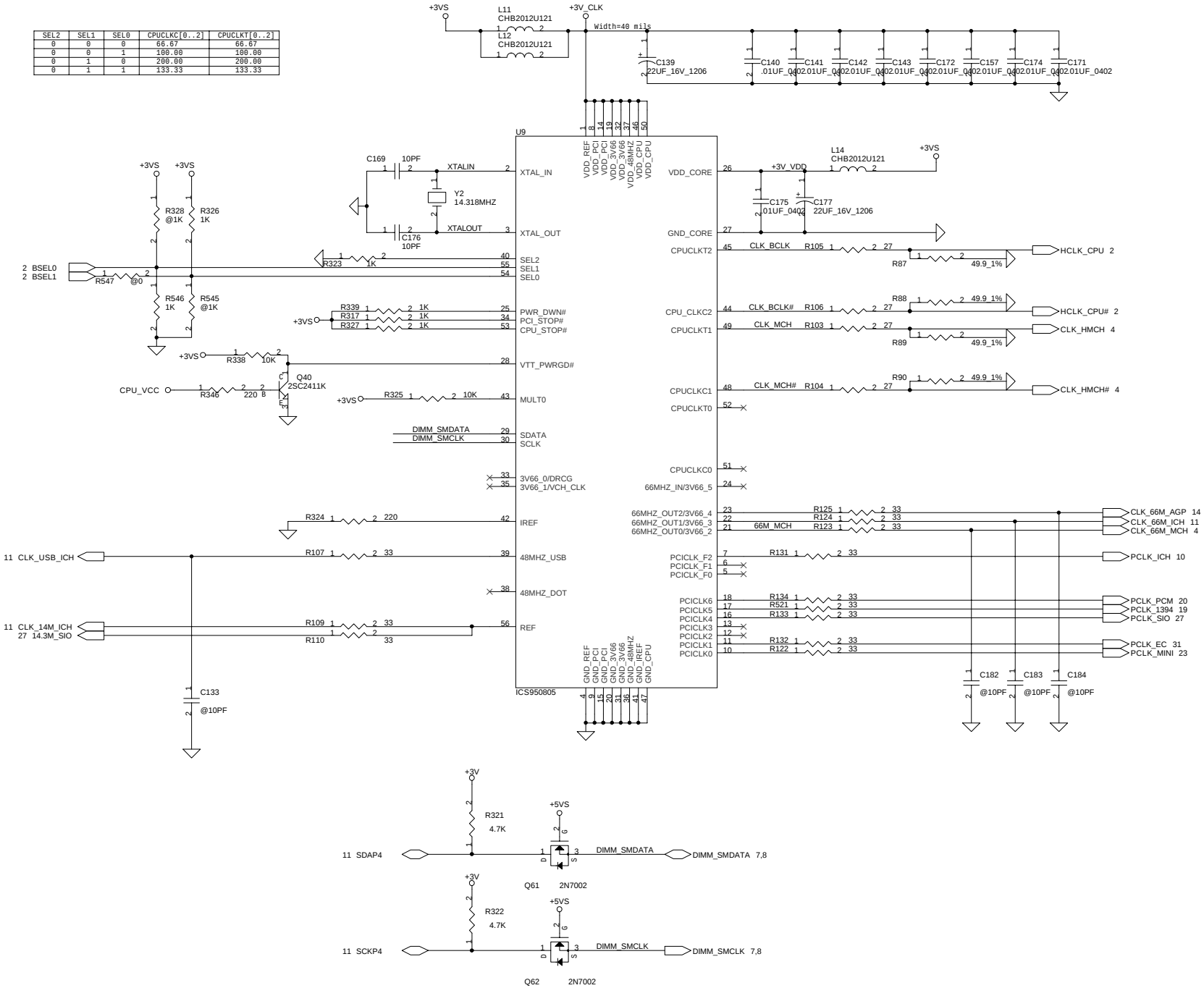
ICH2-C(LAN,Power)

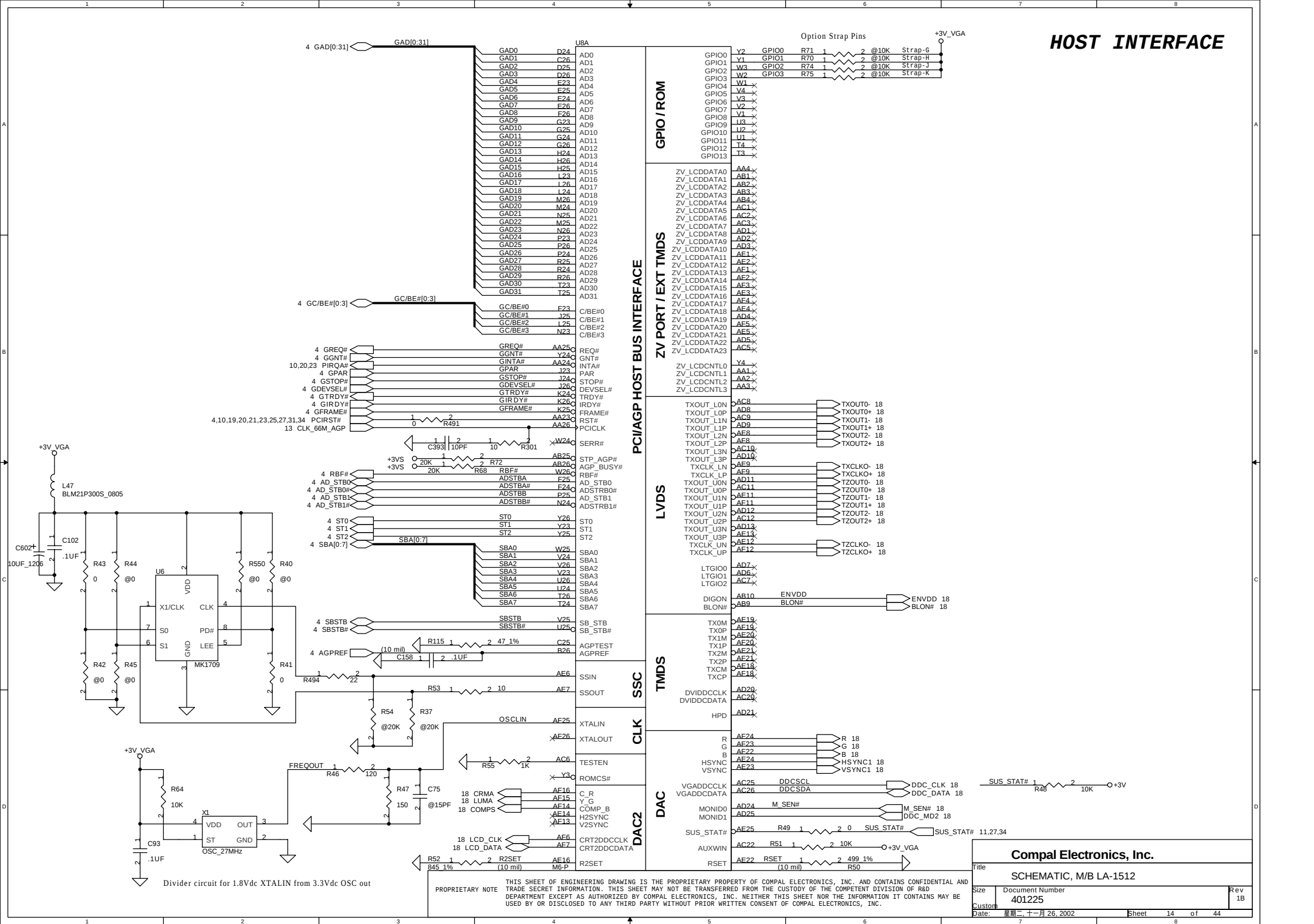


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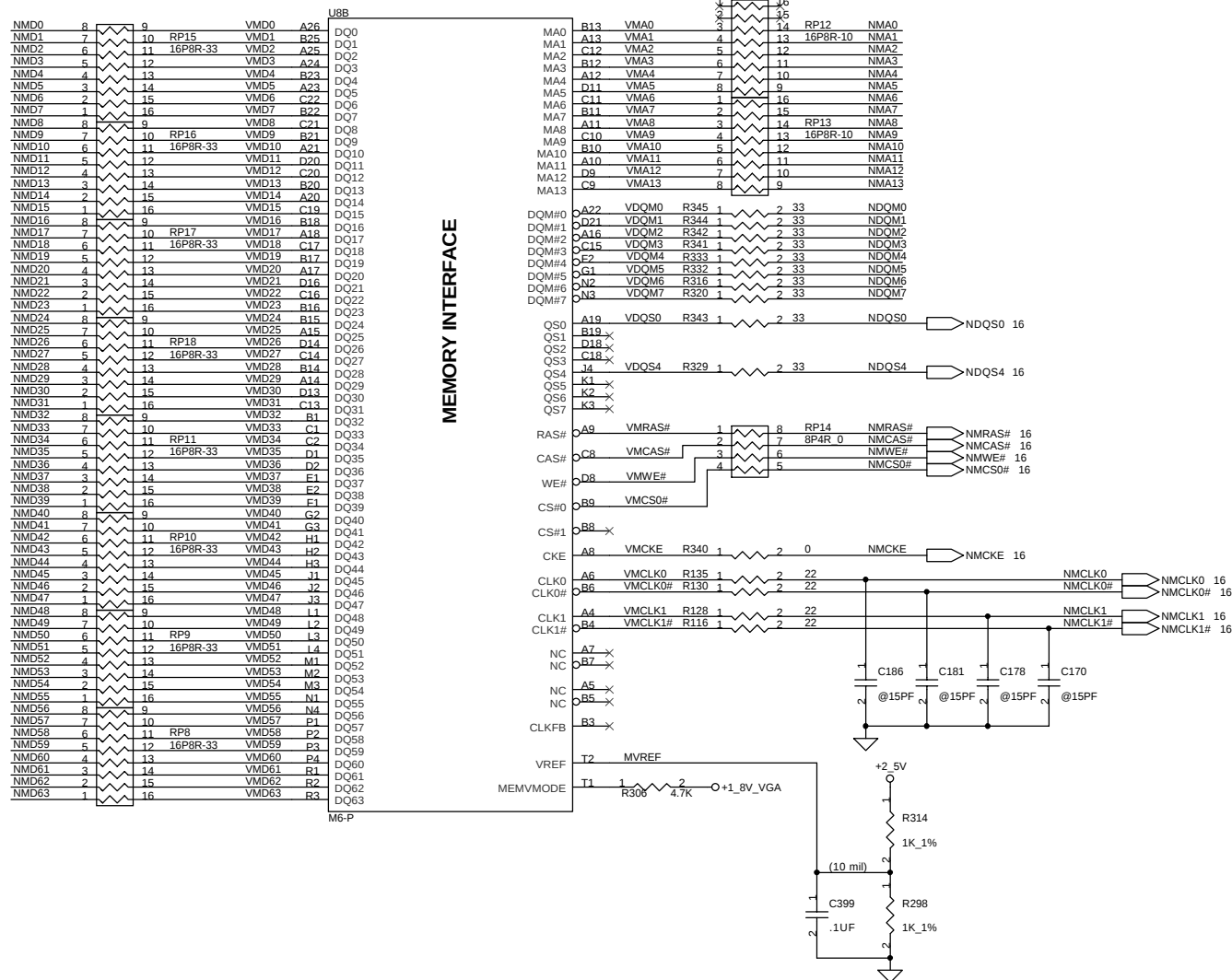
SEL2	SEL1	SEL0	CPUCLK[0..2]	CPUCLKT[0..2]
0	0	0	66.67	66.67
0	0	1	100.00	100.00
0	1	0	200.00	200.00
0	1	1	133.33	133.33





MEMORY INTERFACE

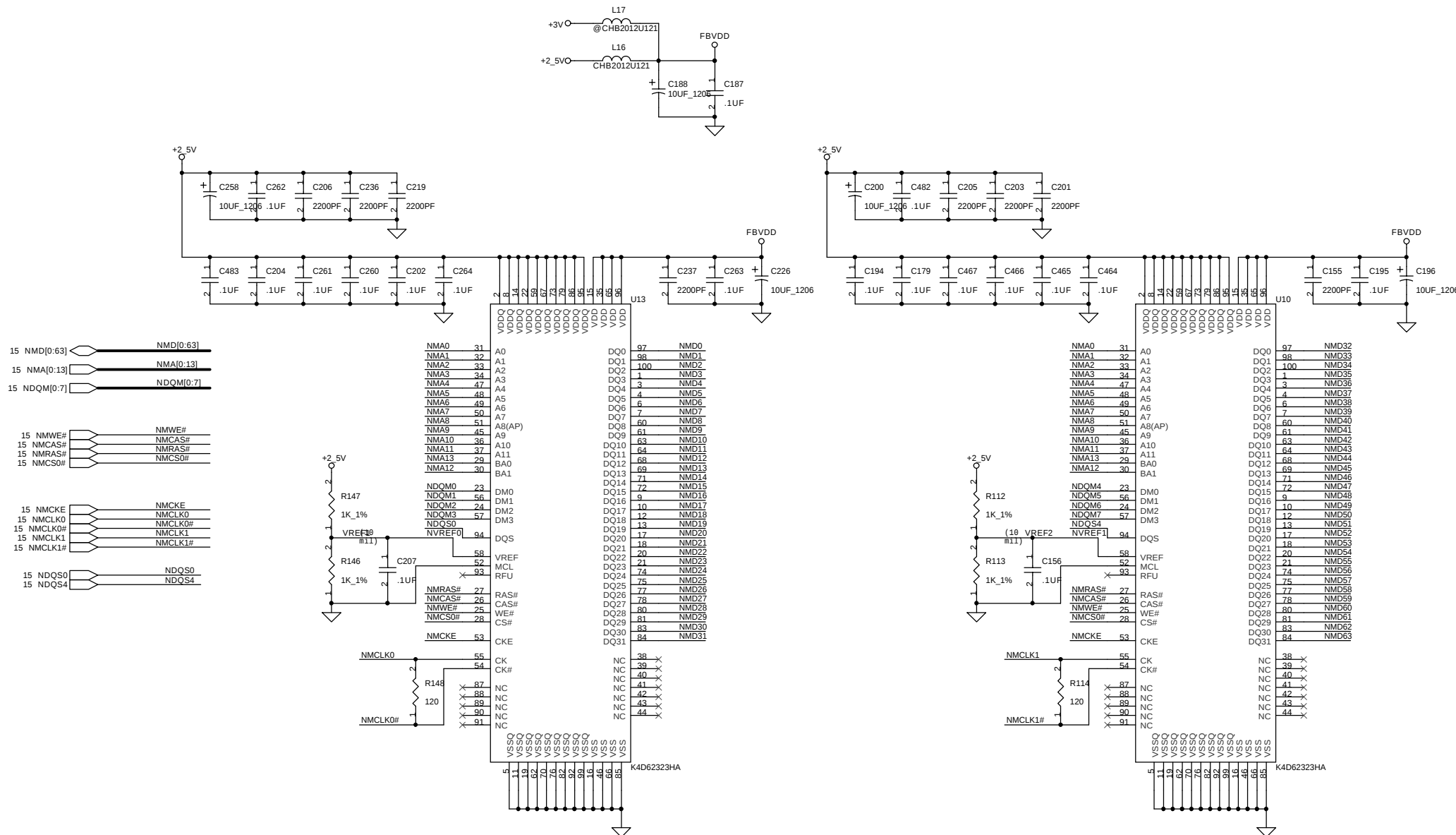
NMD[0:63] 16
NMA[0:13] 16
NDQM[0:7] 16



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DDR SDRAM



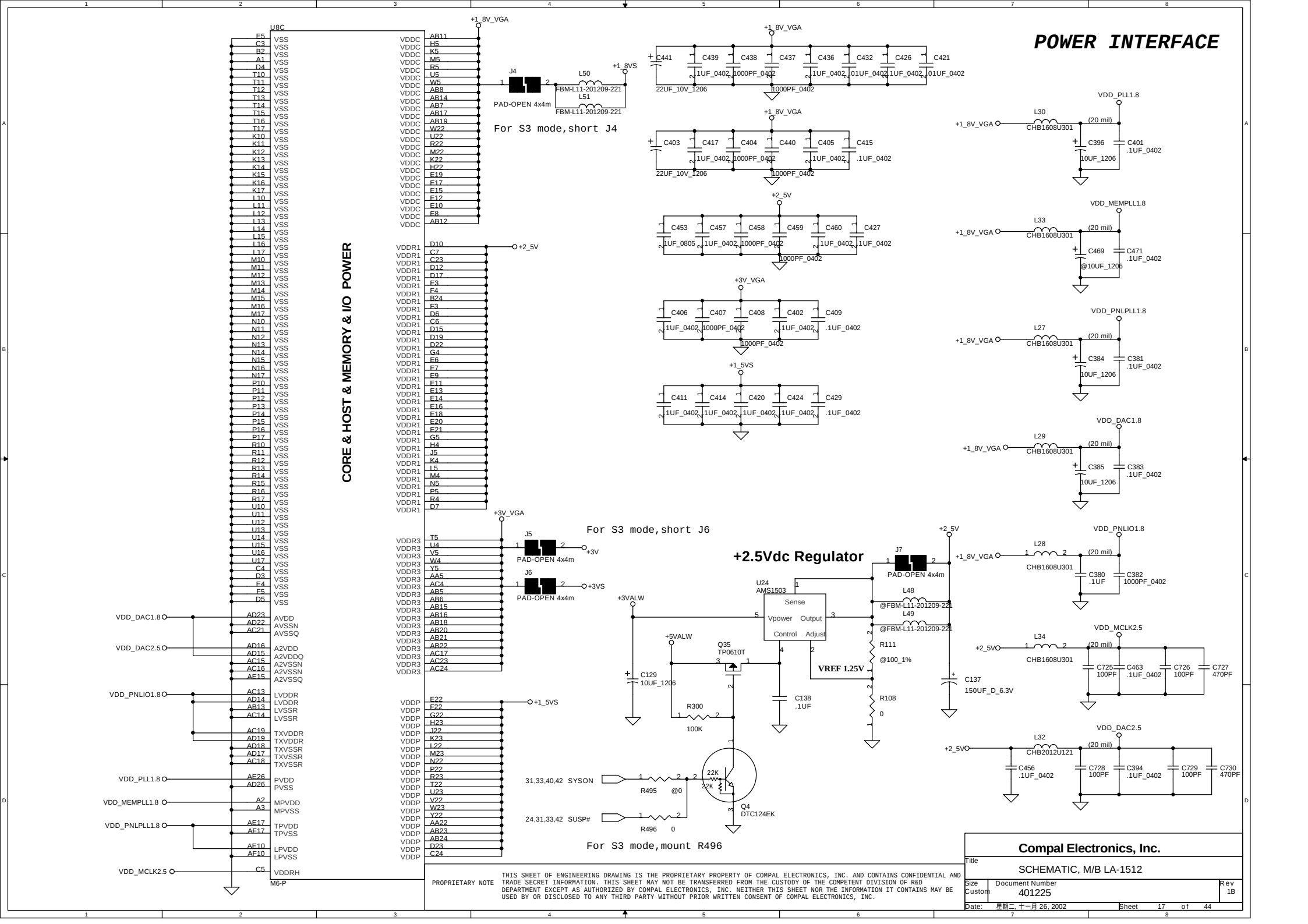
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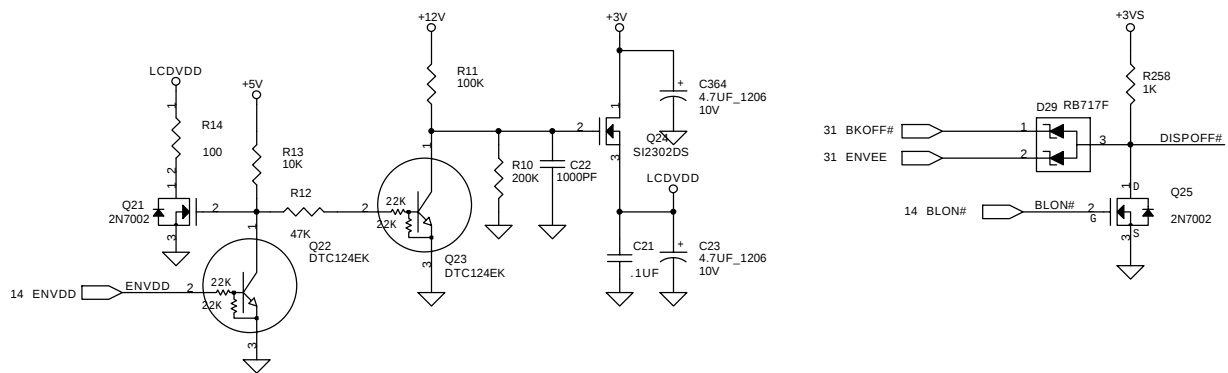
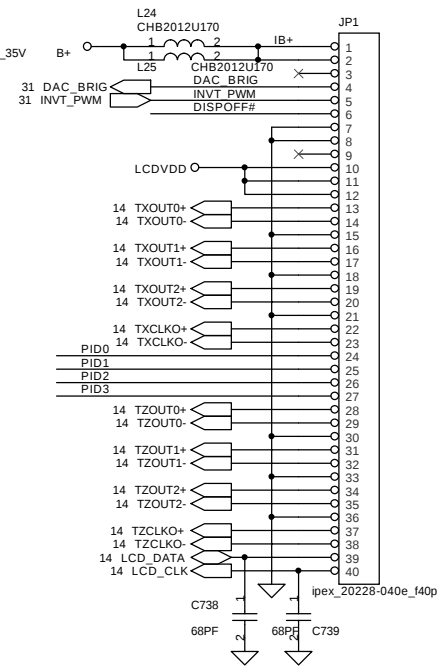
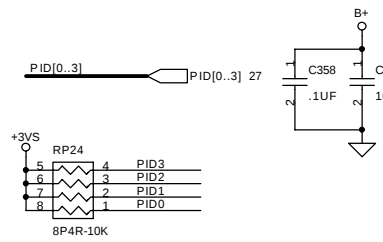
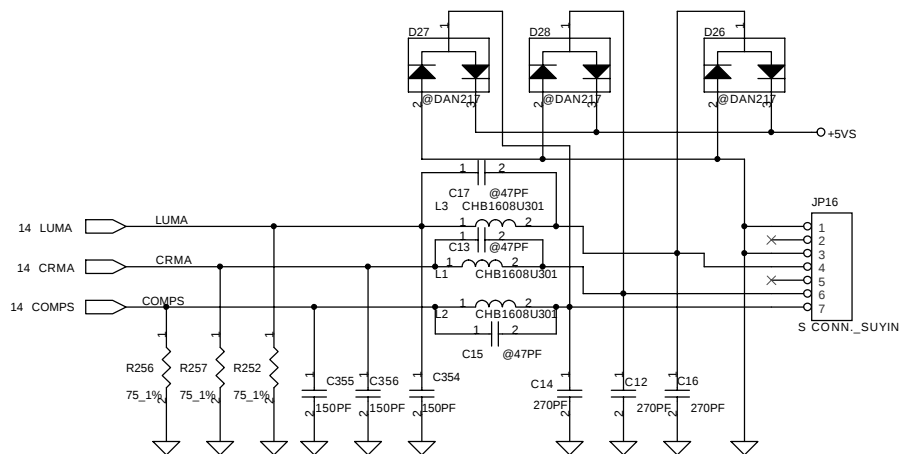
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Size Document Number Rev
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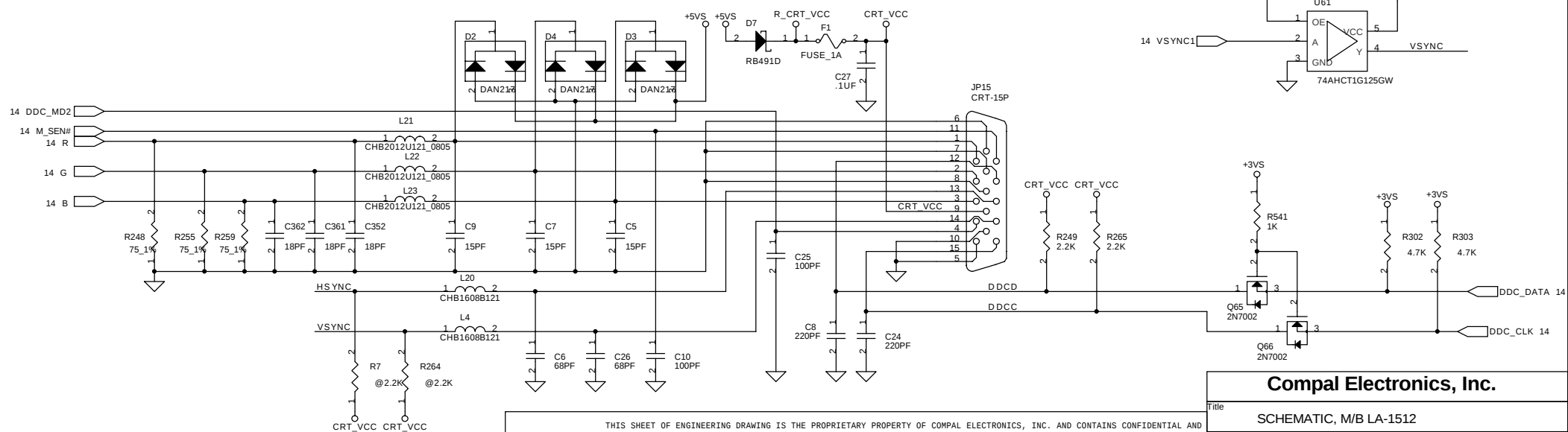
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CRT Connector

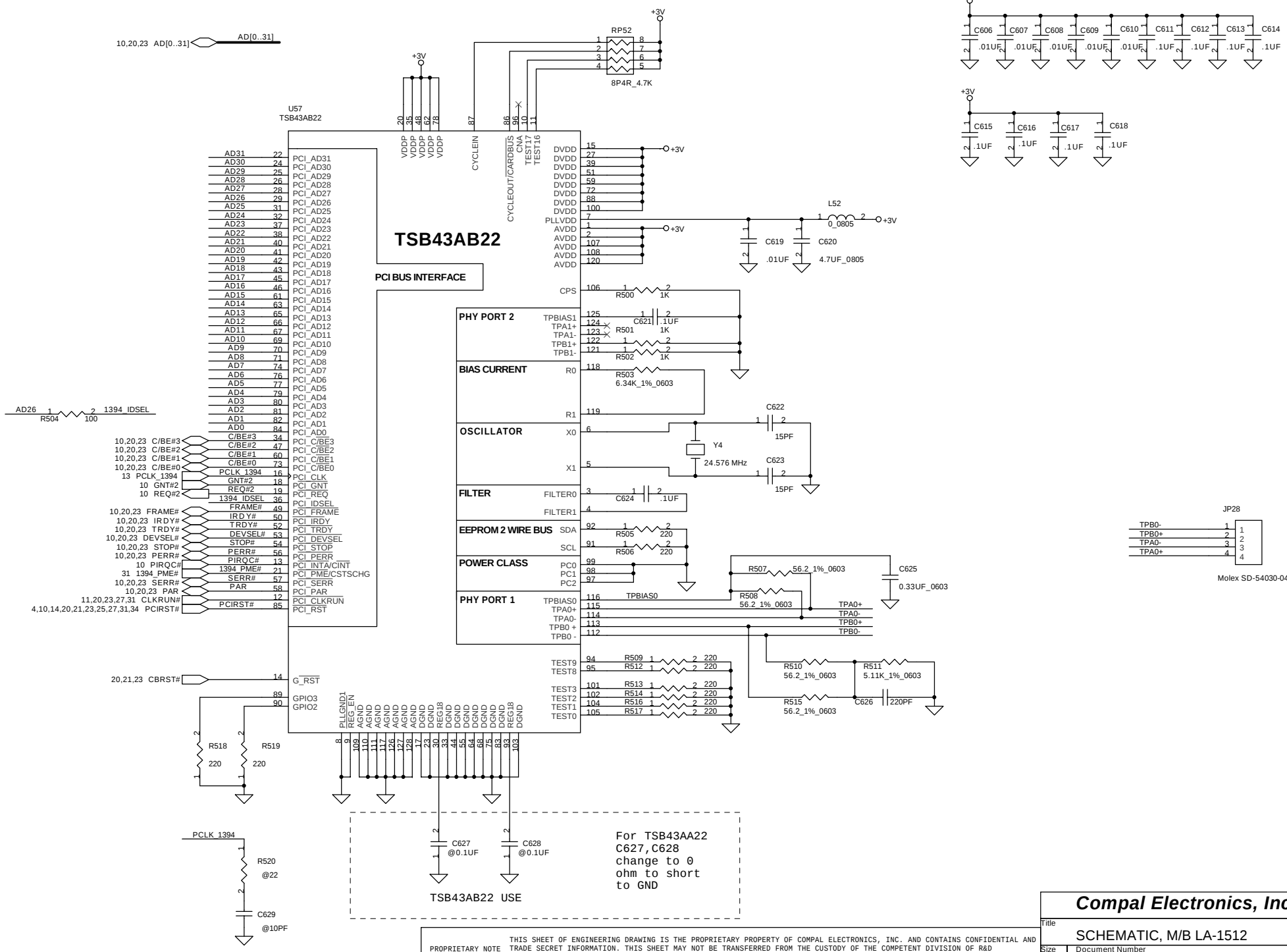


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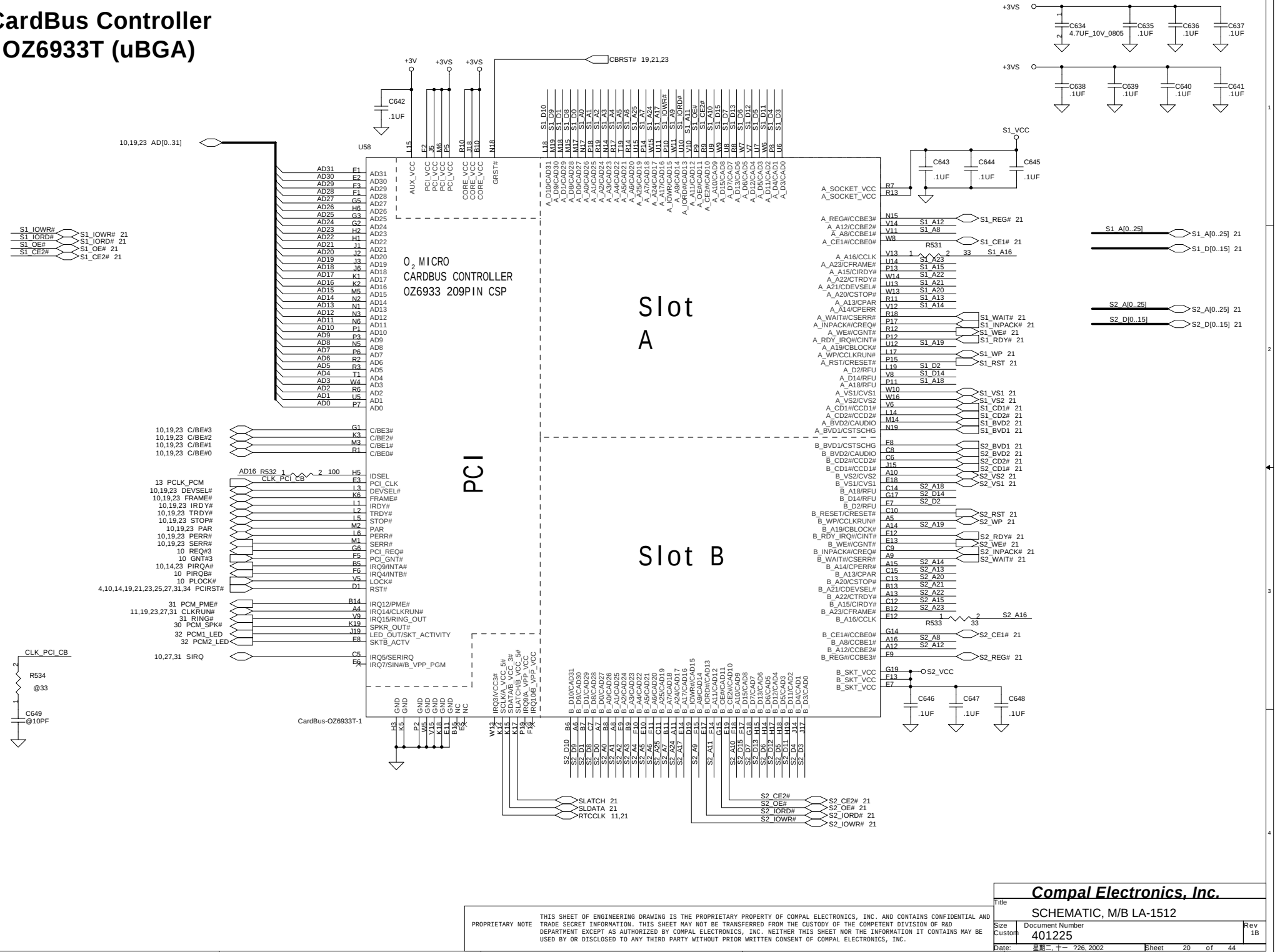
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CardBus Controller
OZ6933T (uBGA)

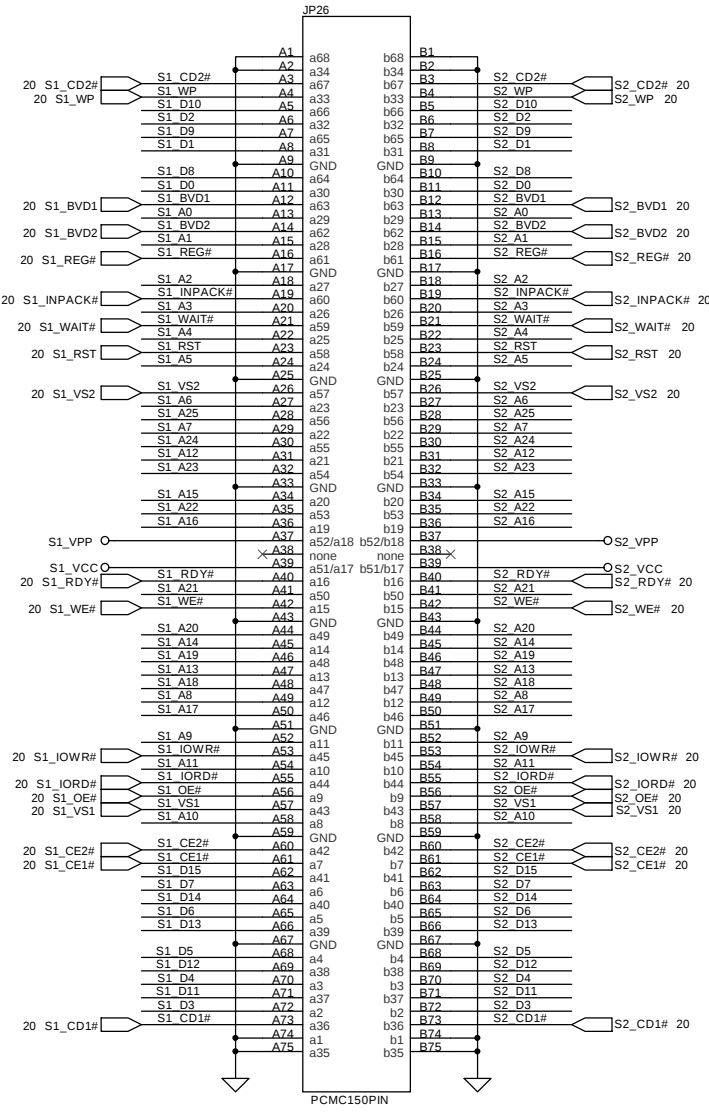
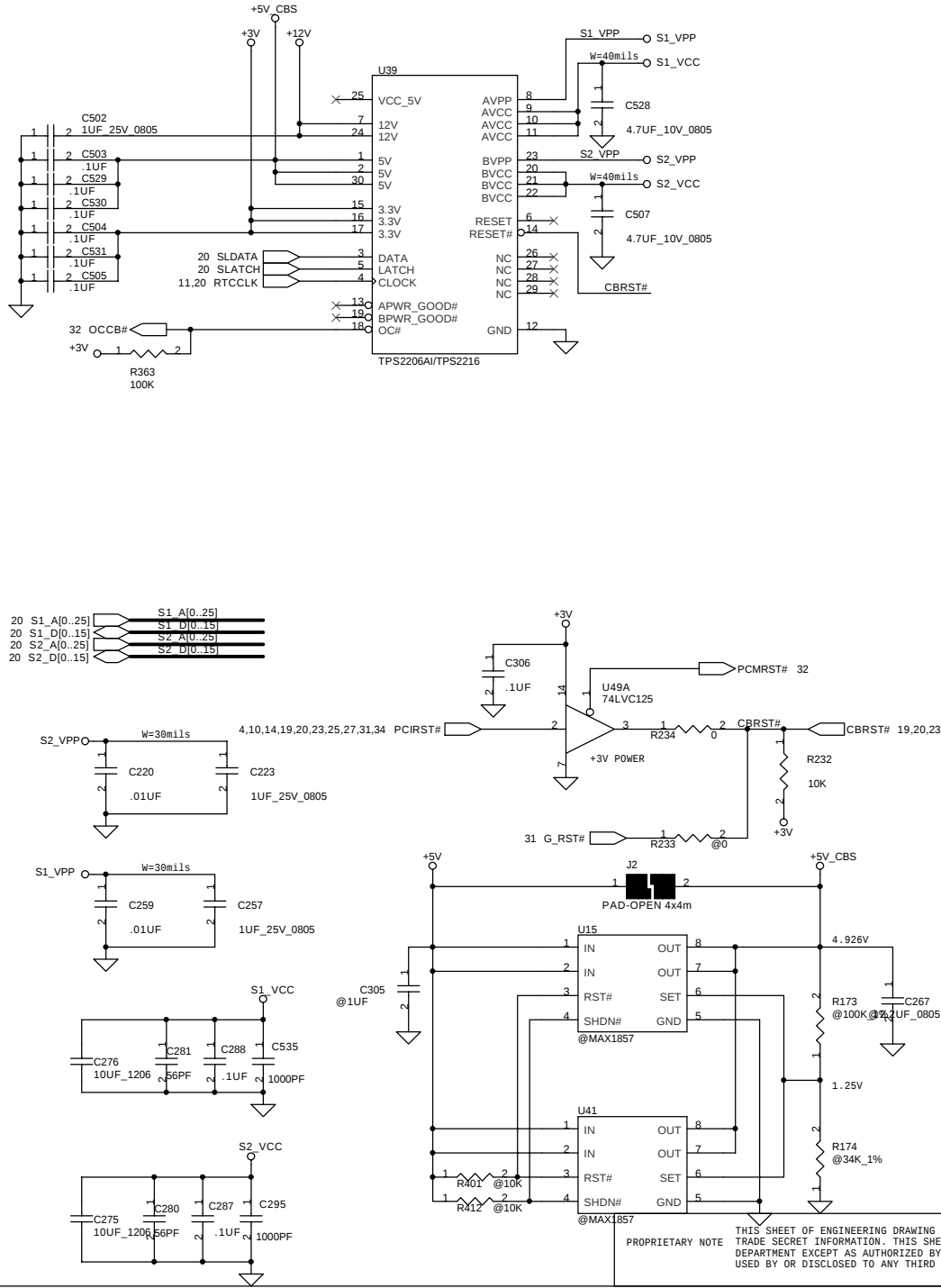


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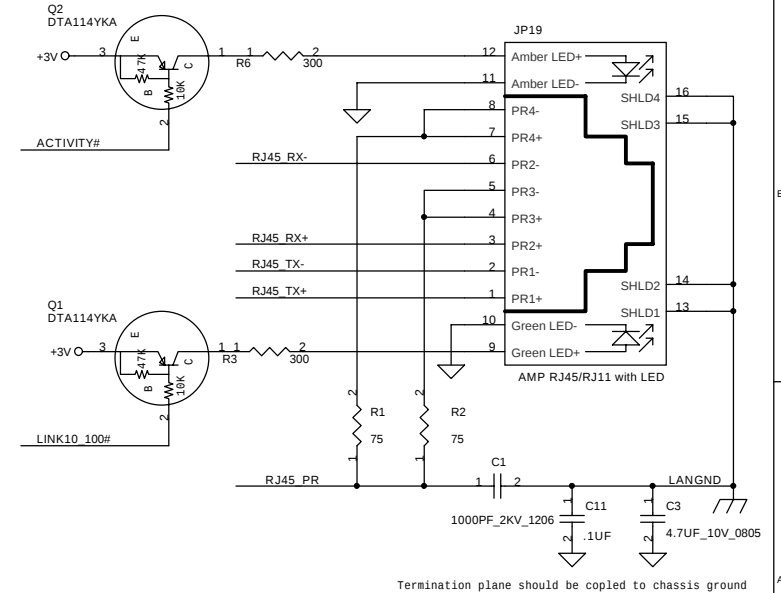
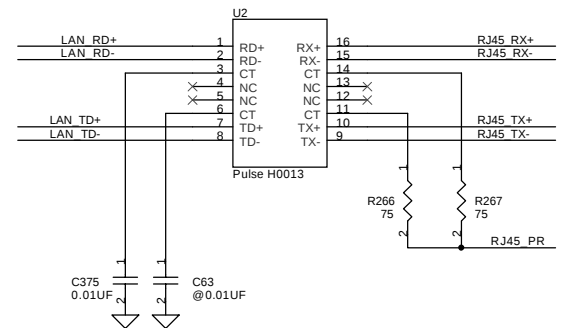
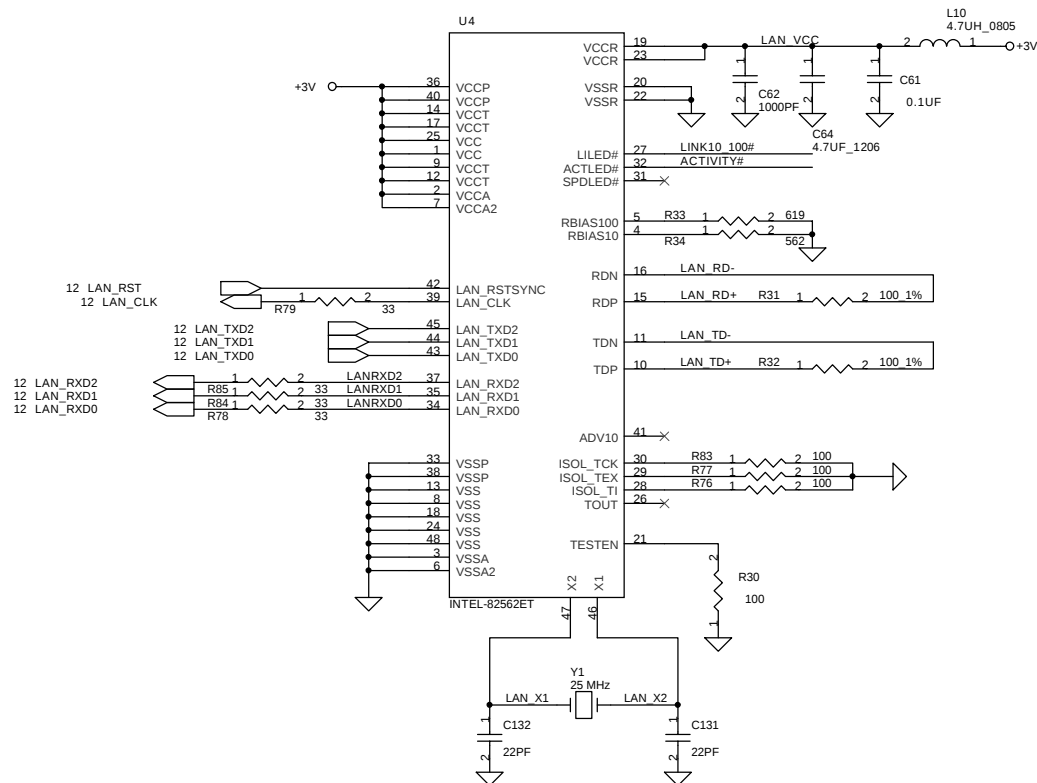
PCMCIA POWER CTRL.

CARDBUS
SOCKET



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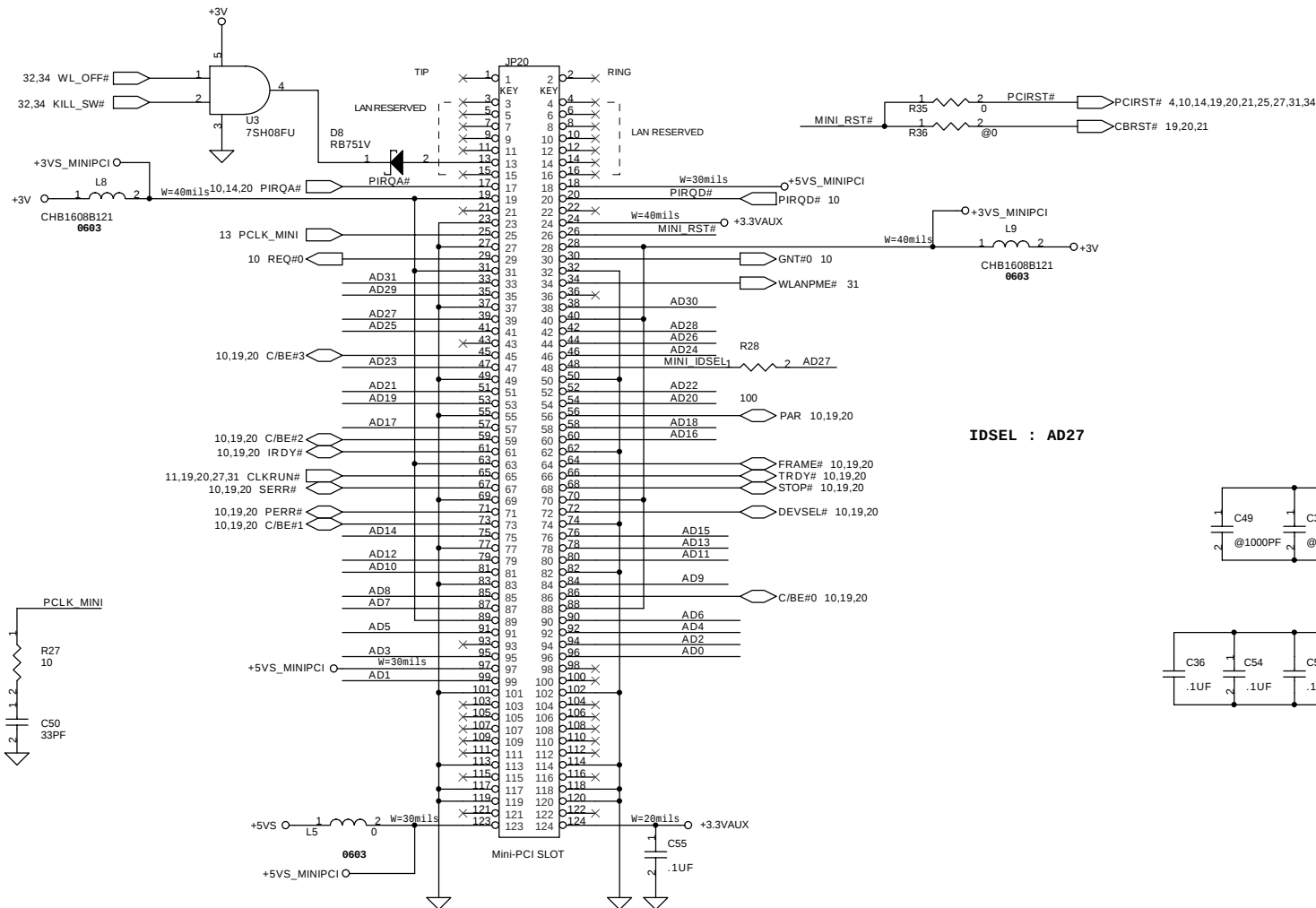
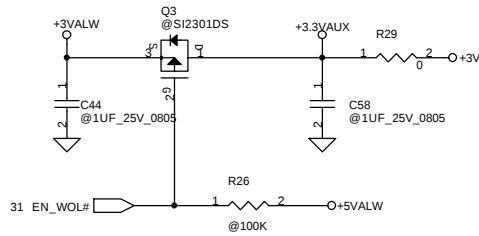
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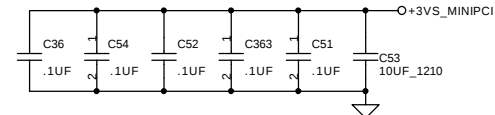
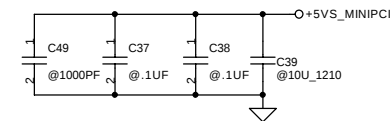
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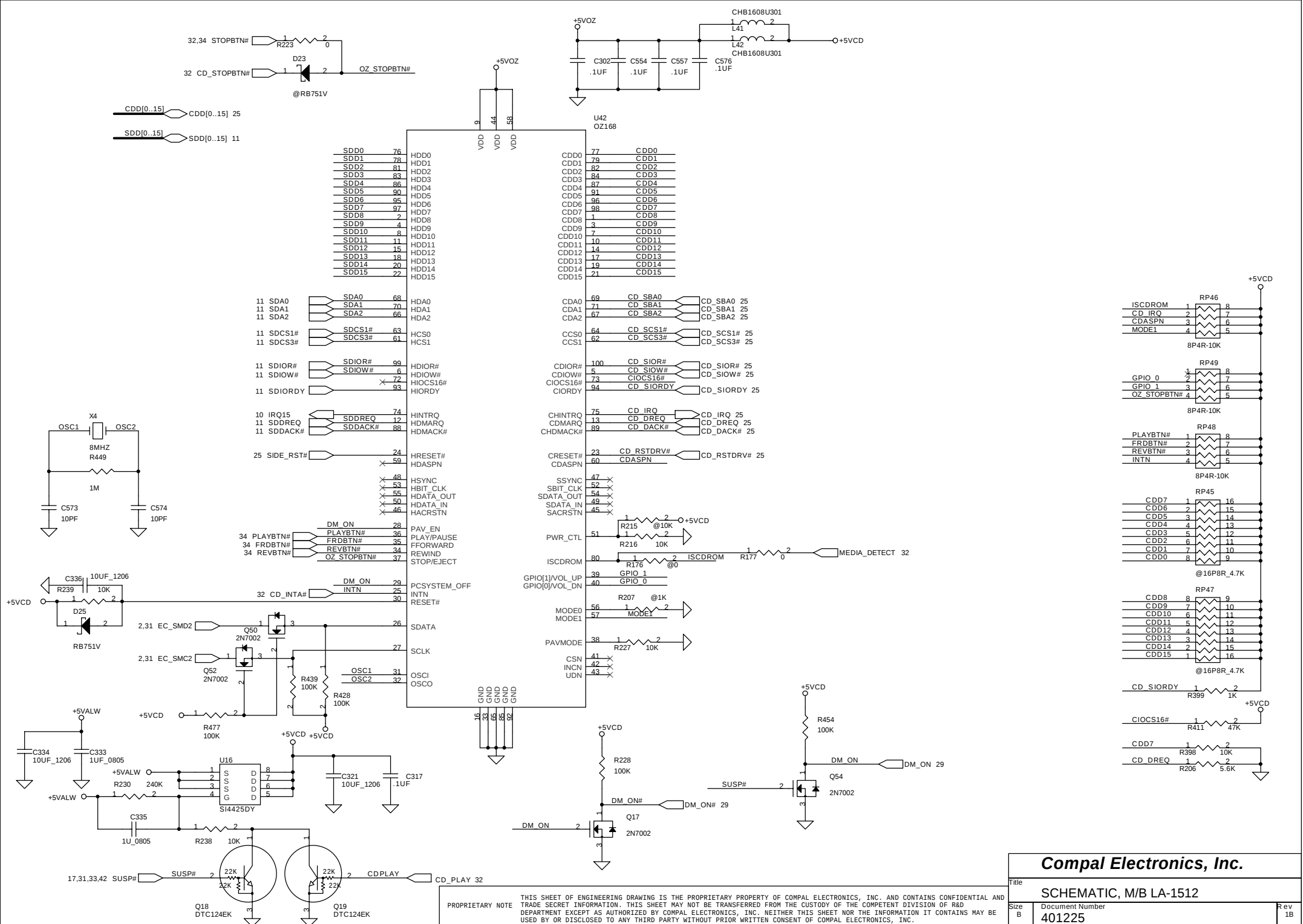


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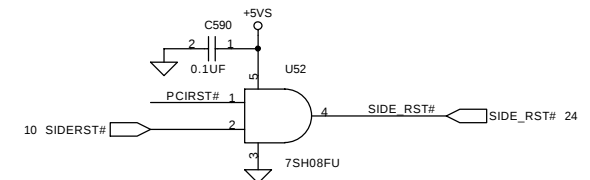
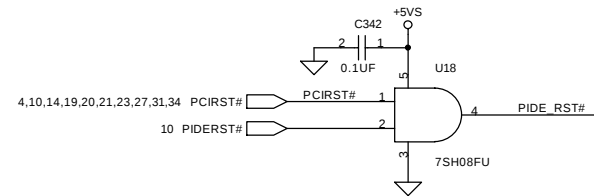
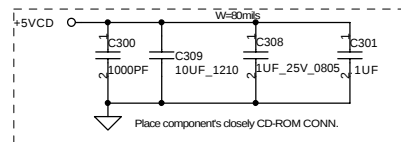
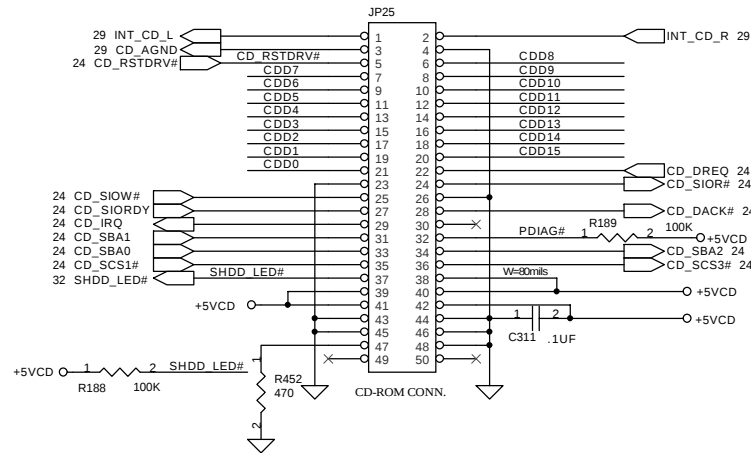
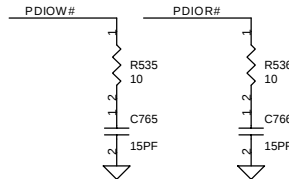
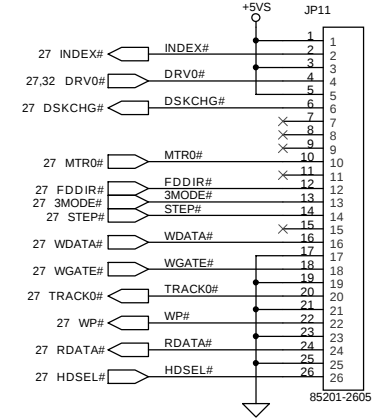
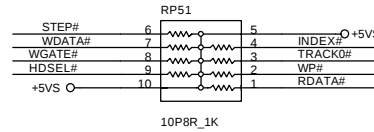
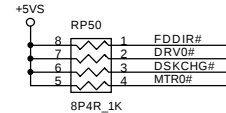
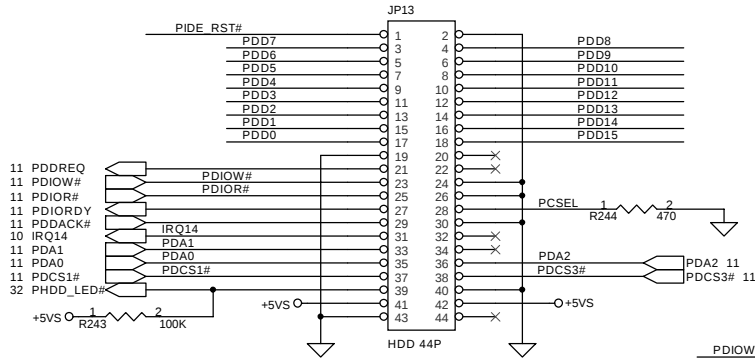
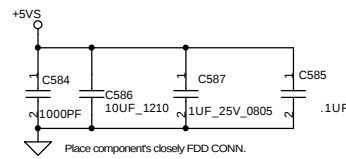
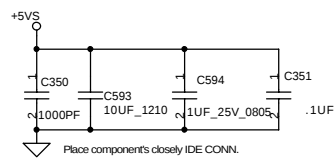


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IDE,CD-ROM Module CONN.



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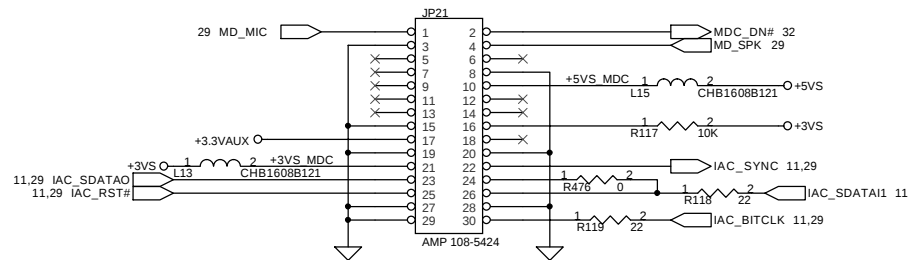
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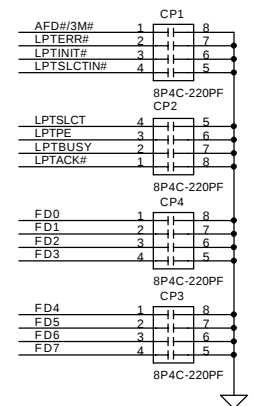
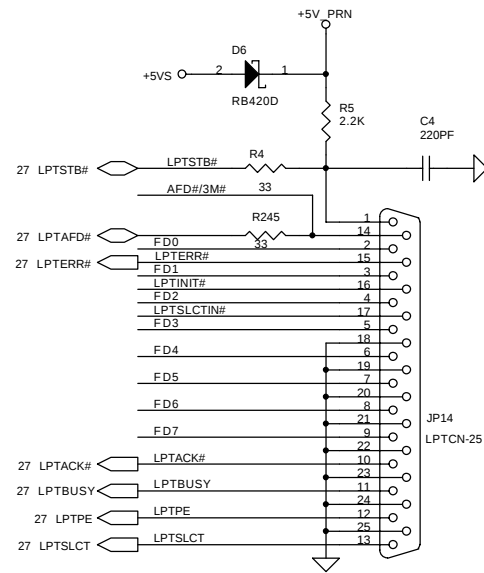
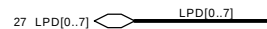
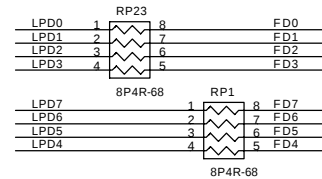
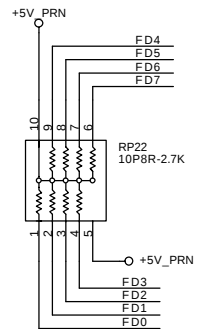
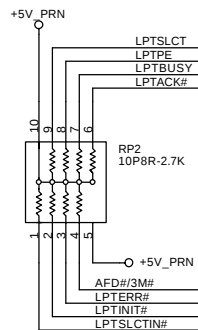
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PARALLEL PORT

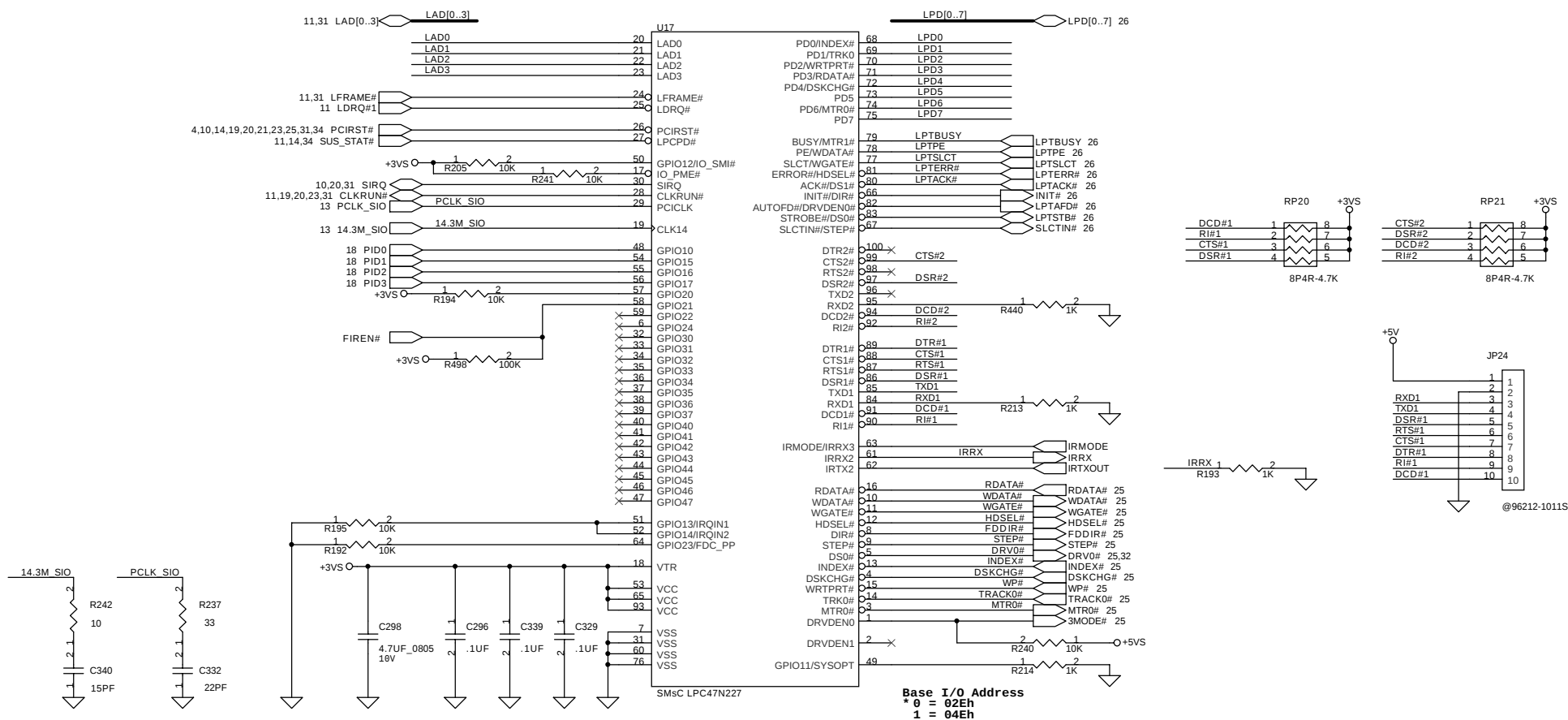


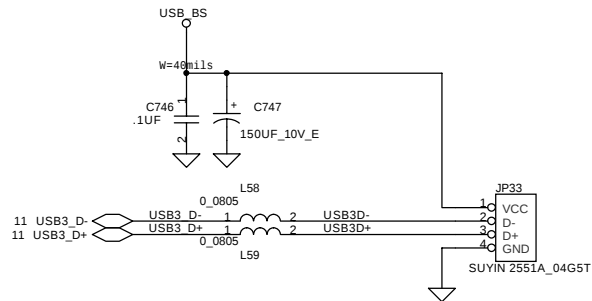
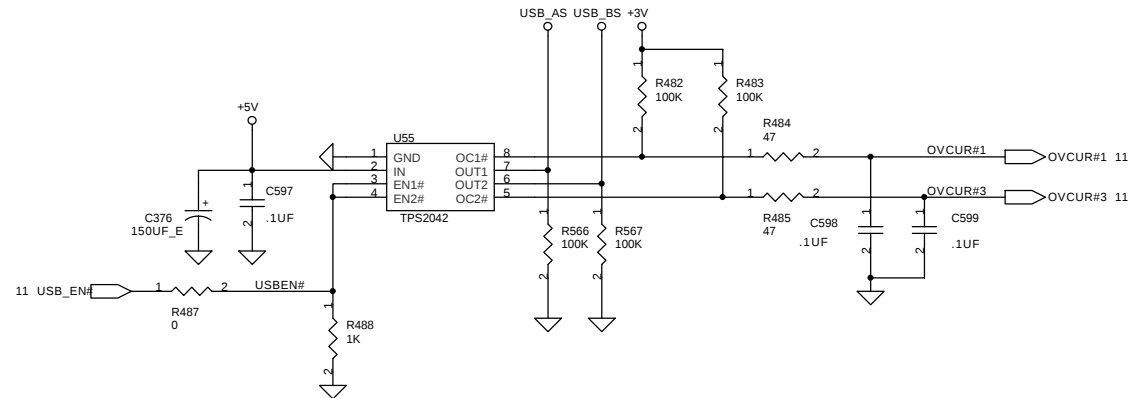
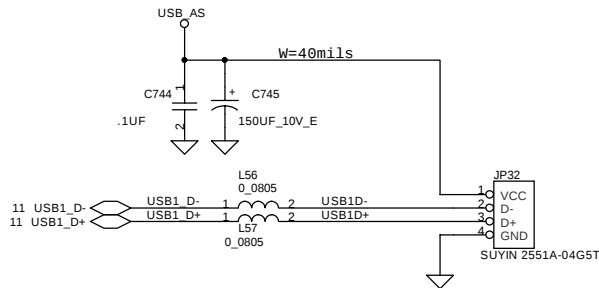
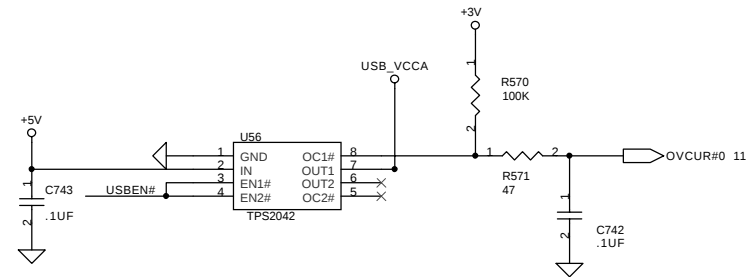
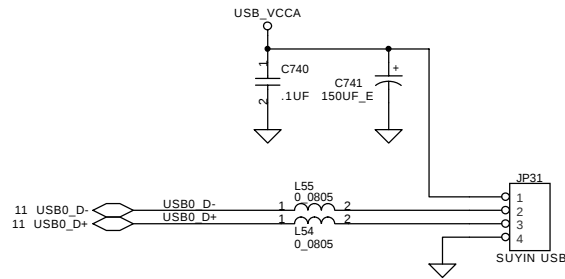
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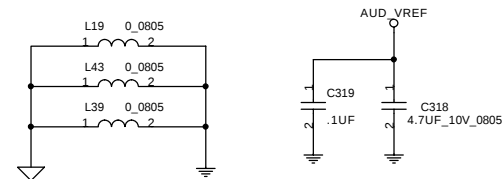
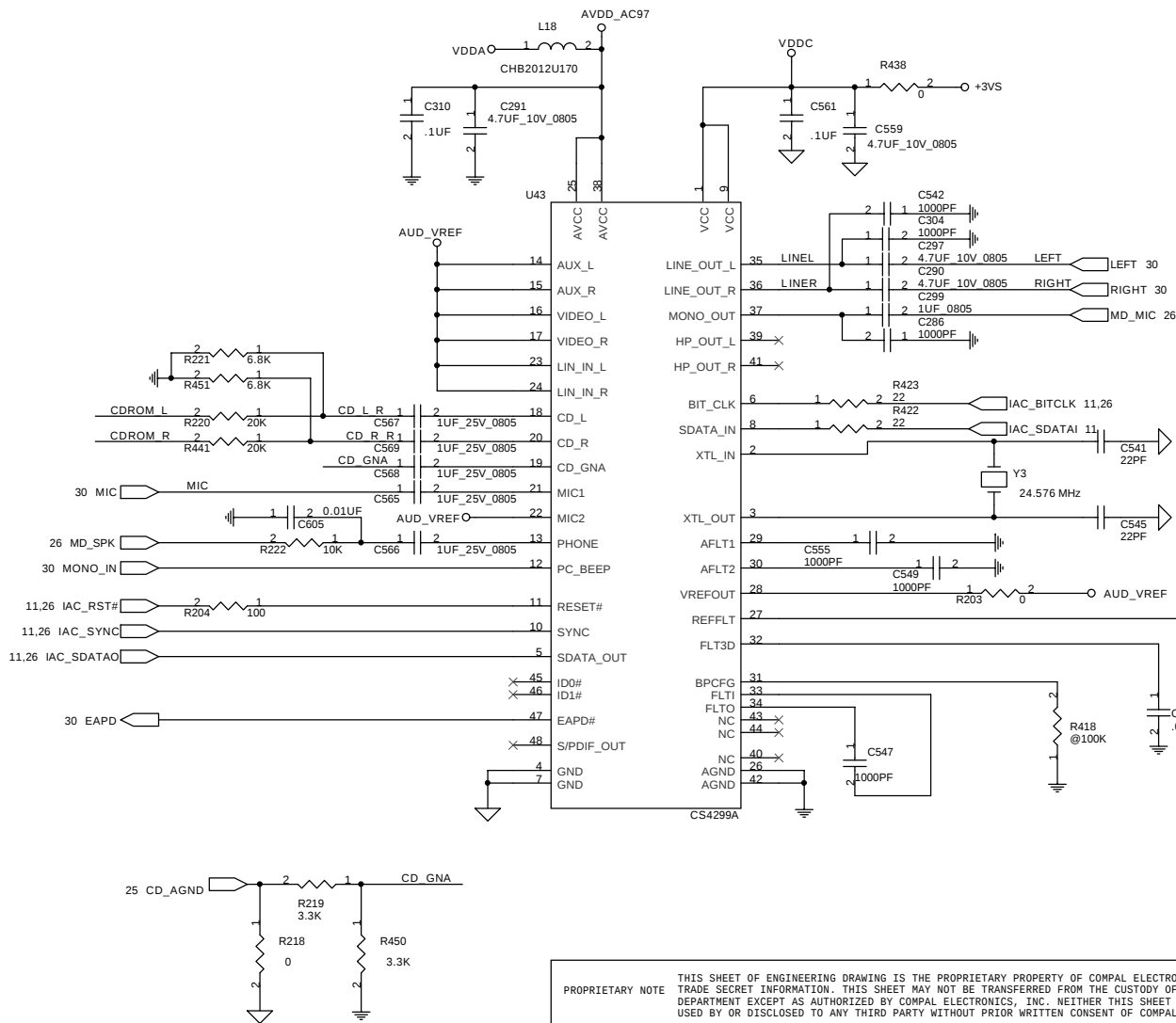
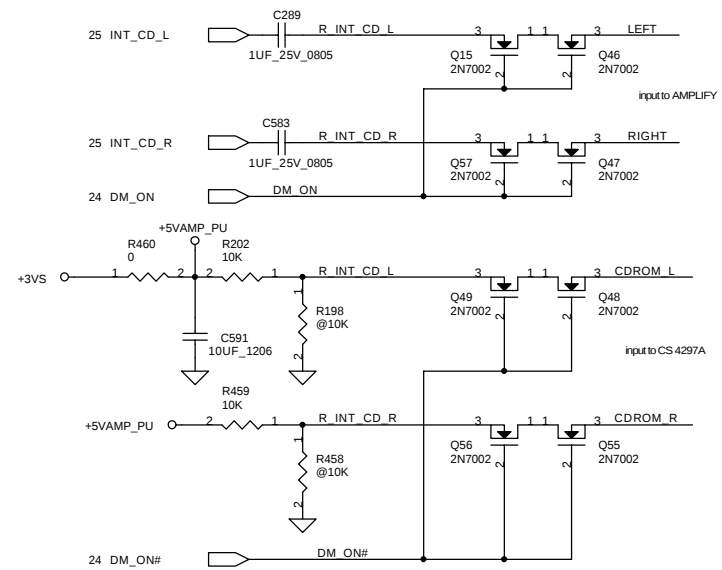
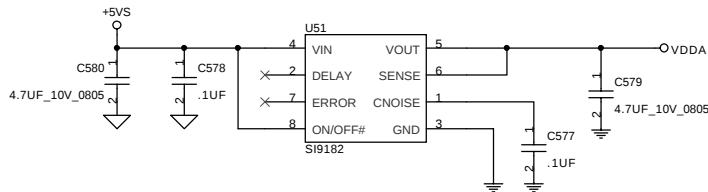
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AC97 Codec



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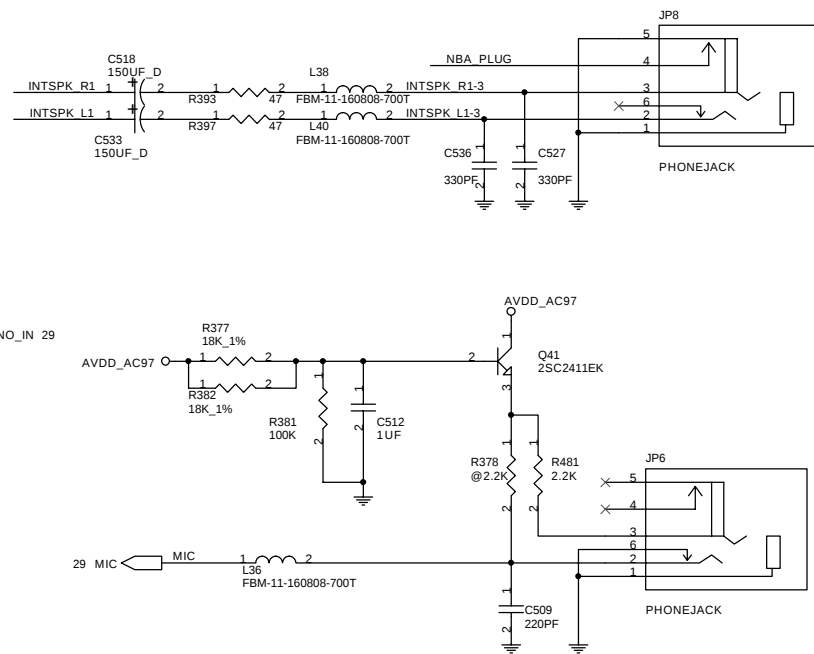
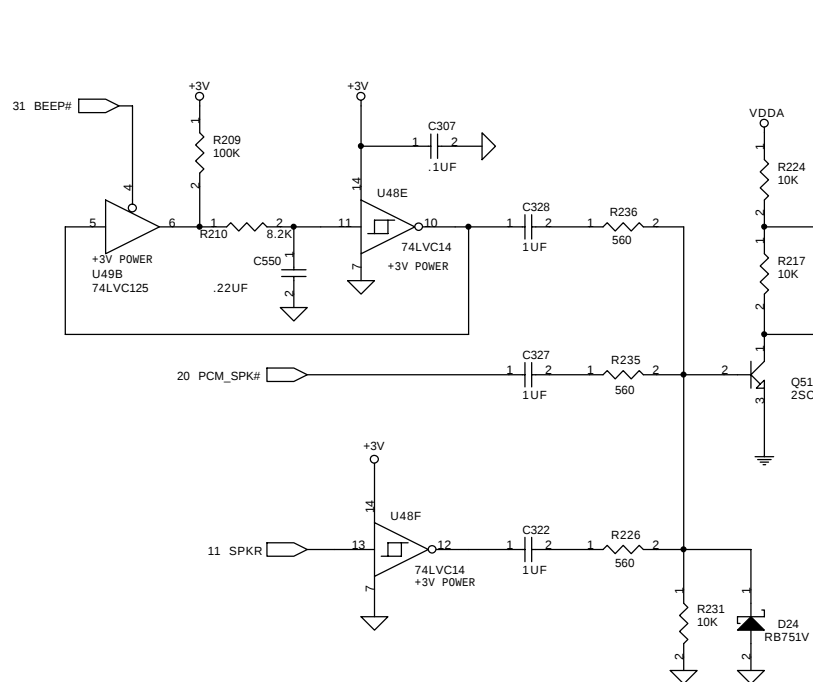
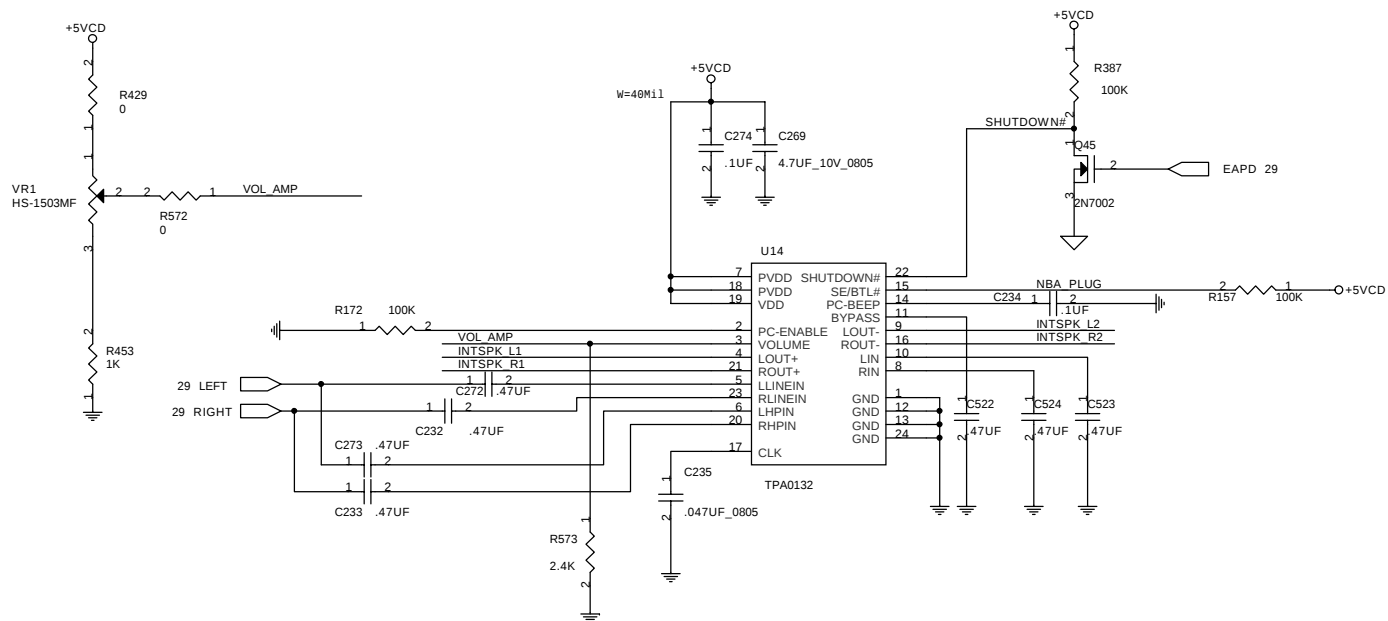
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AMP & Audio Jack

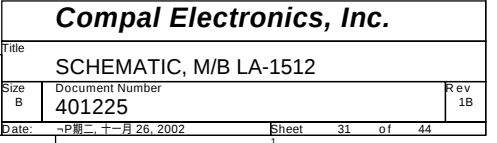


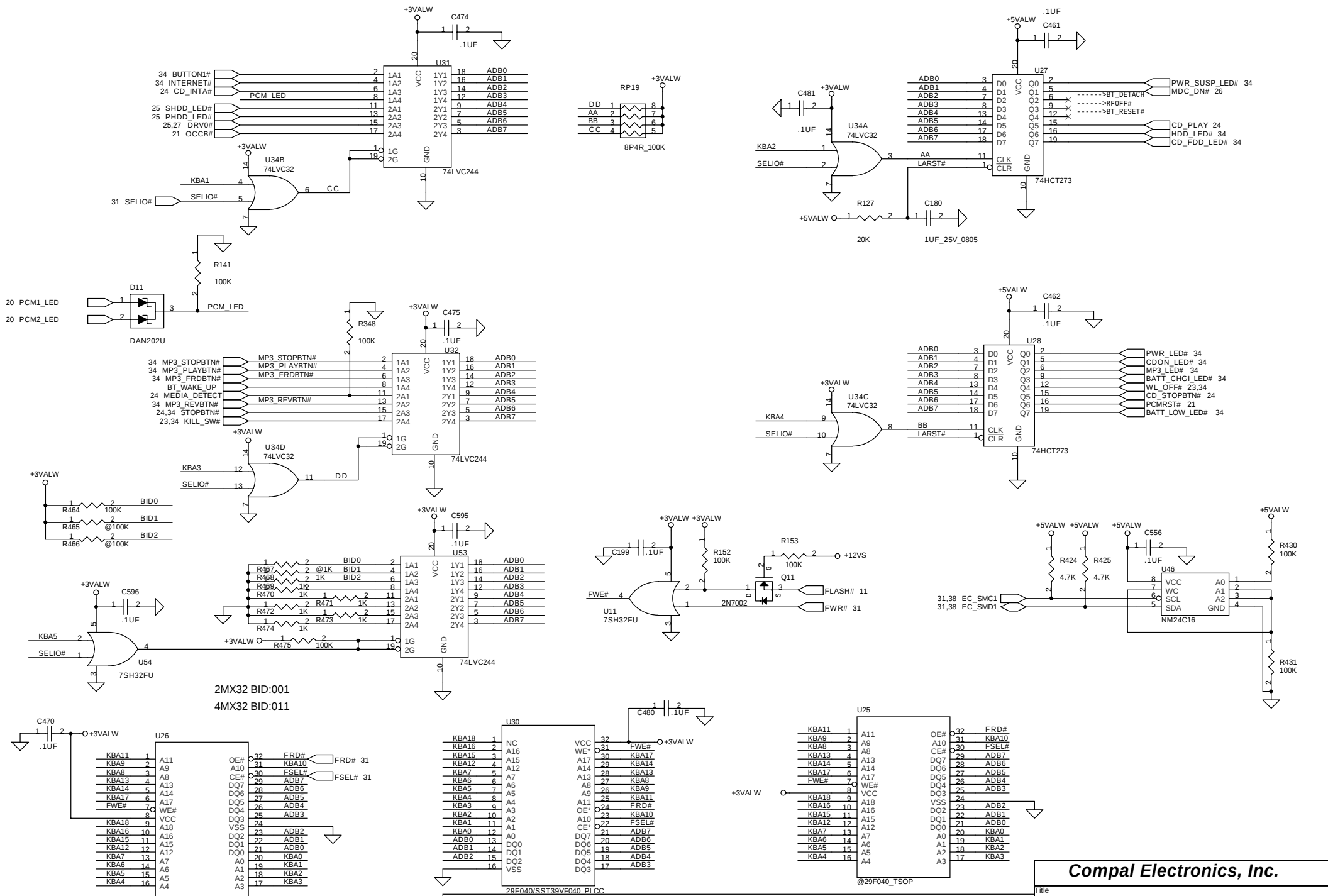
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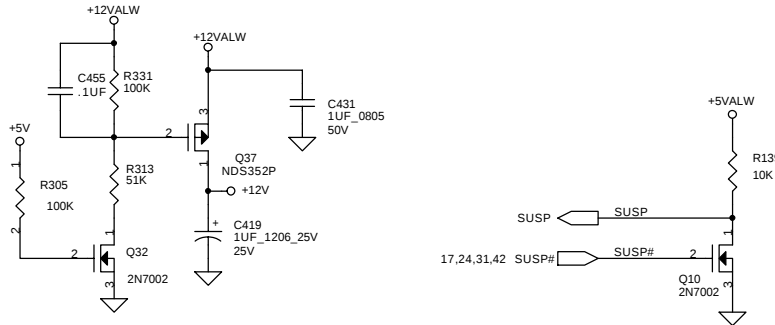
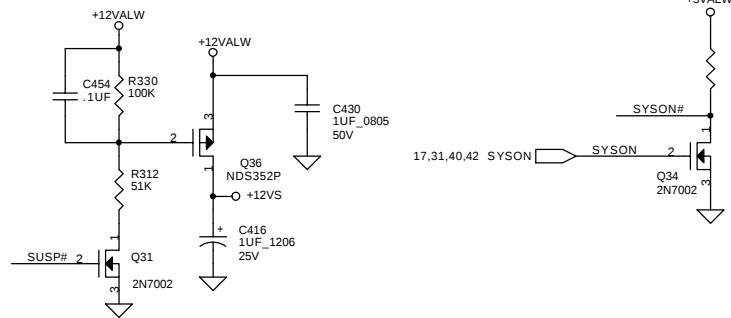
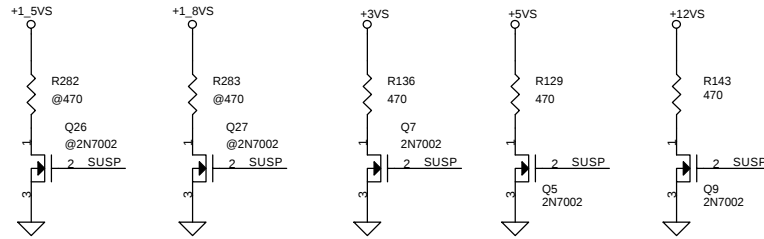
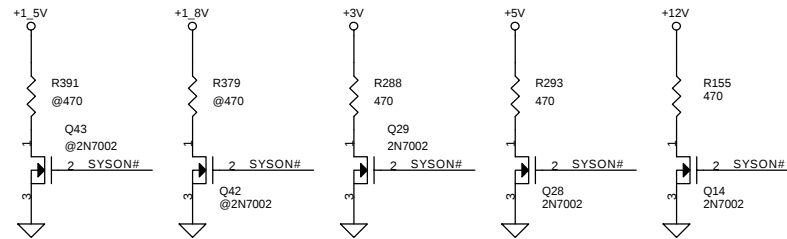
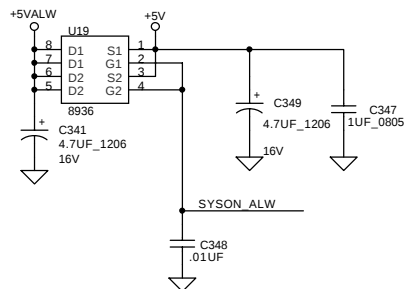
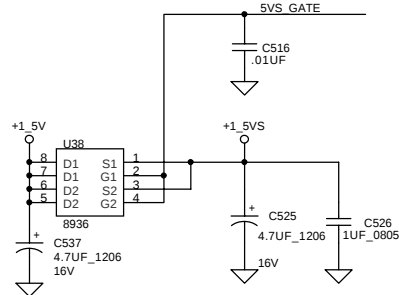
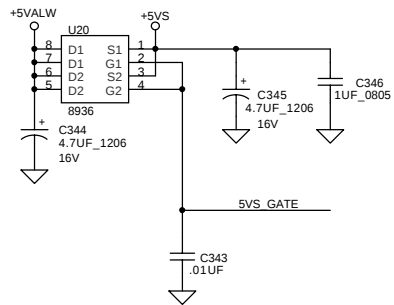
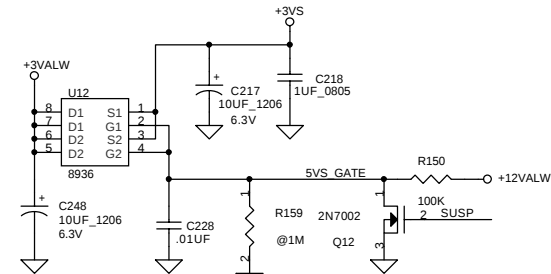
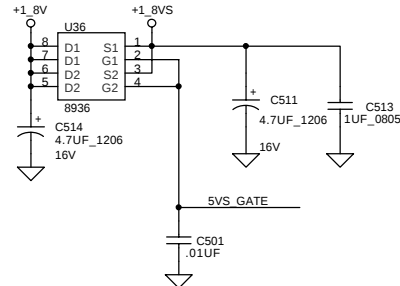
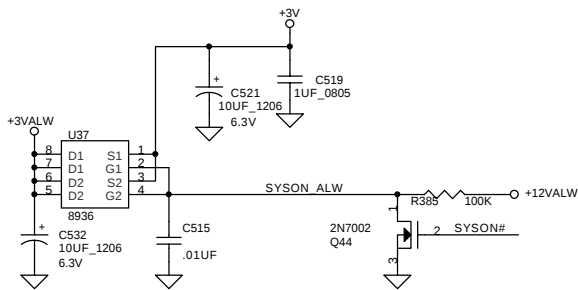
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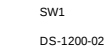
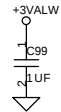
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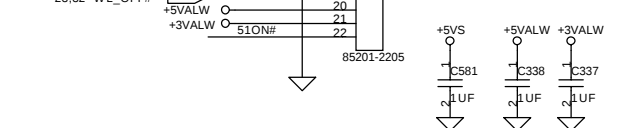
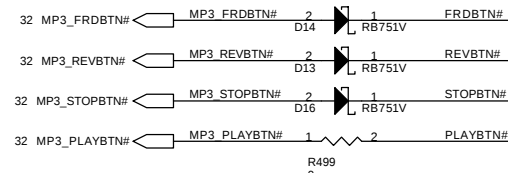
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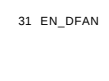
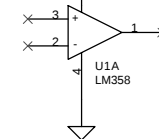
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FAN CONN.



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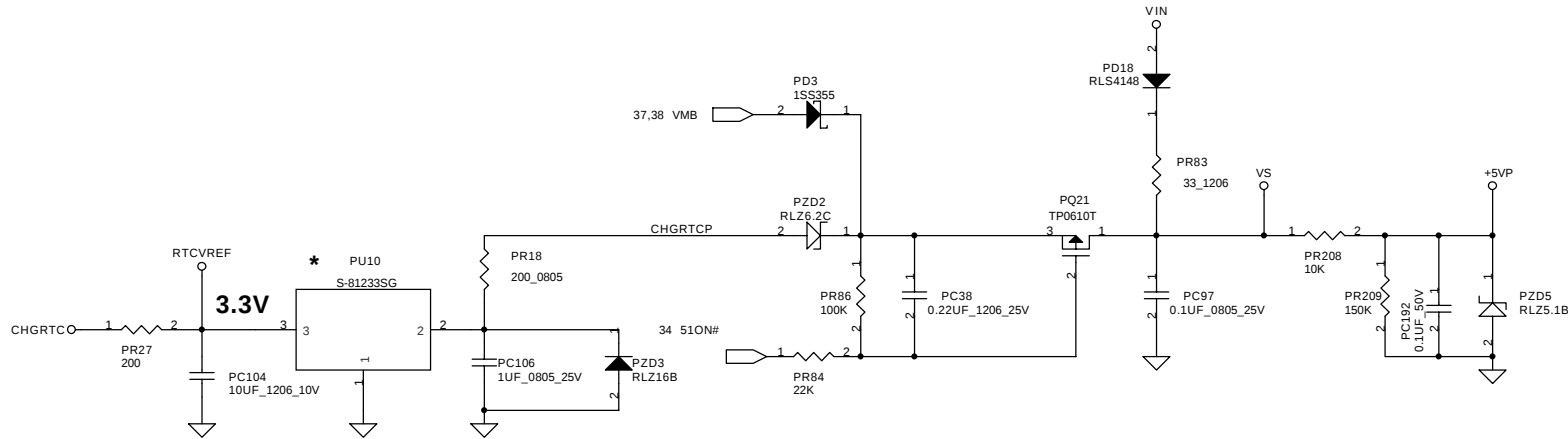
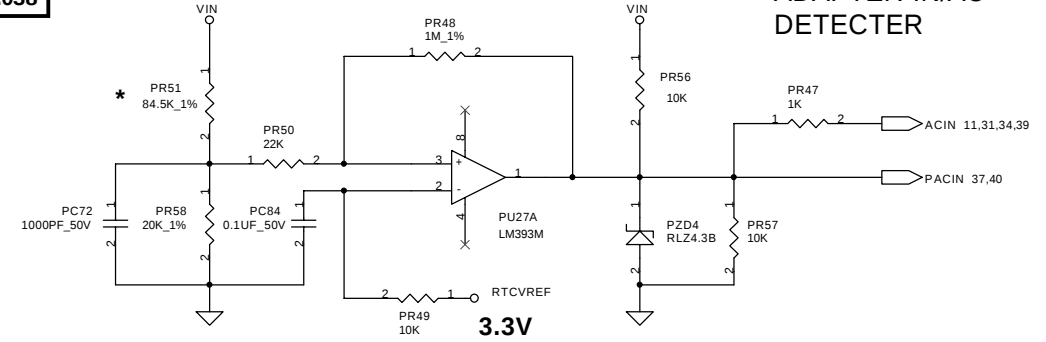
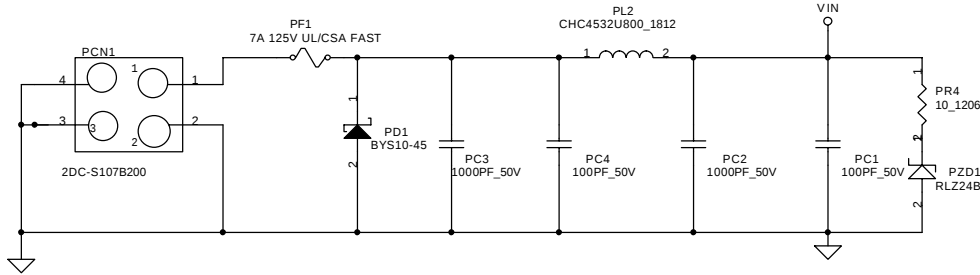
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Vin Detector

High 18.764 17.901 17.063
Low 17.745 16.903 16.038



+1.8VP 1 2 3MMA (3A,120mils ,Via NO.= 6)

+12VALWP 2 1 120mA,20mils ,Via NO.= 1
JOPEN/+12V

+5VALWP 1 2 PAD-OPEN 4x4m (5A,200mils ,Via NO.= 10)

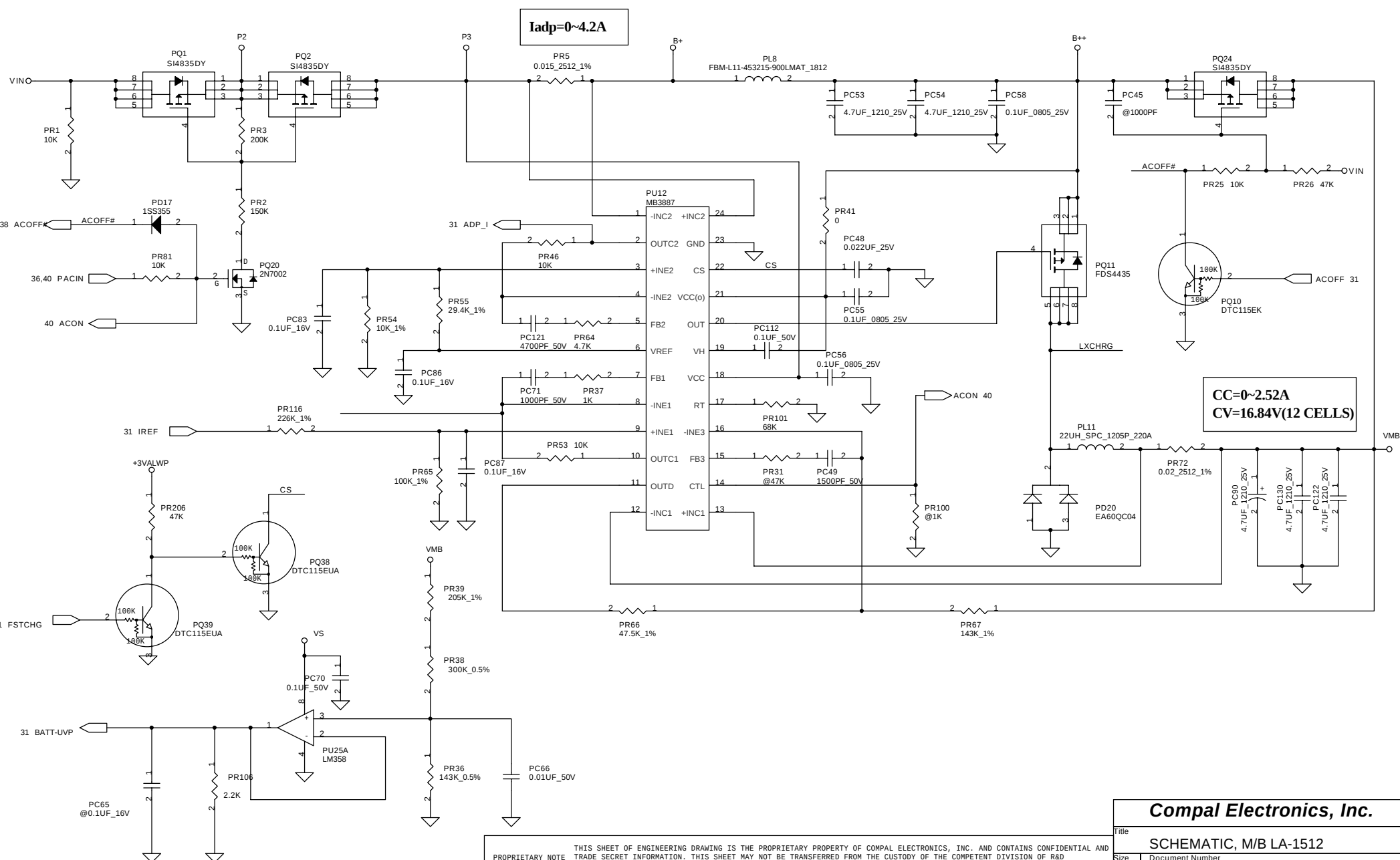
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+1.5VP 2 1 3MMA/CPU_IO (3A,120mils ,Via NO.= 6)

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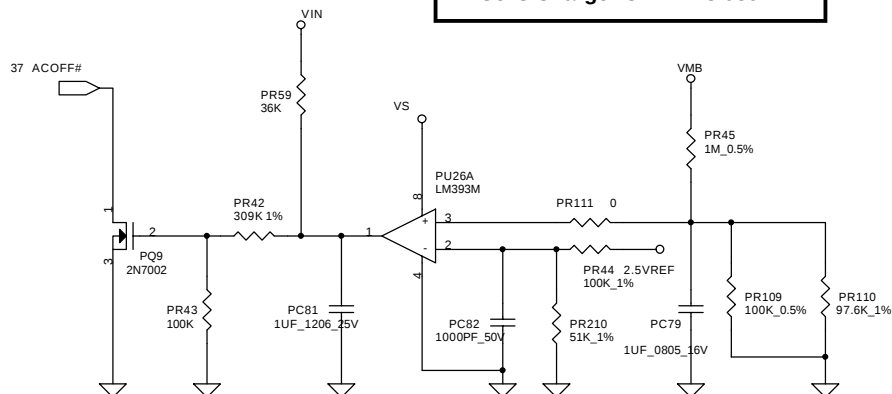


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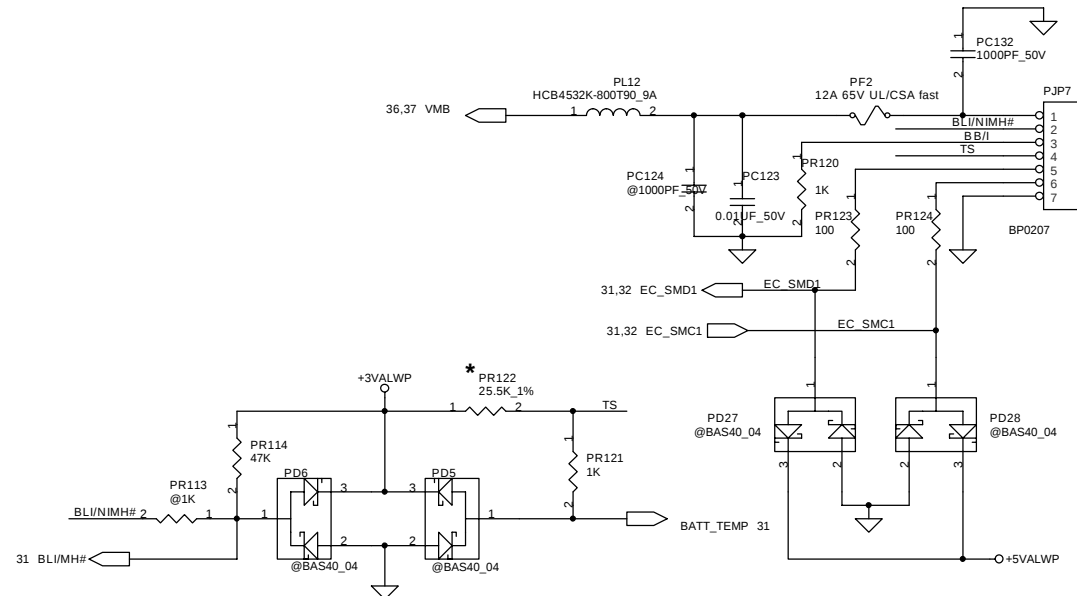
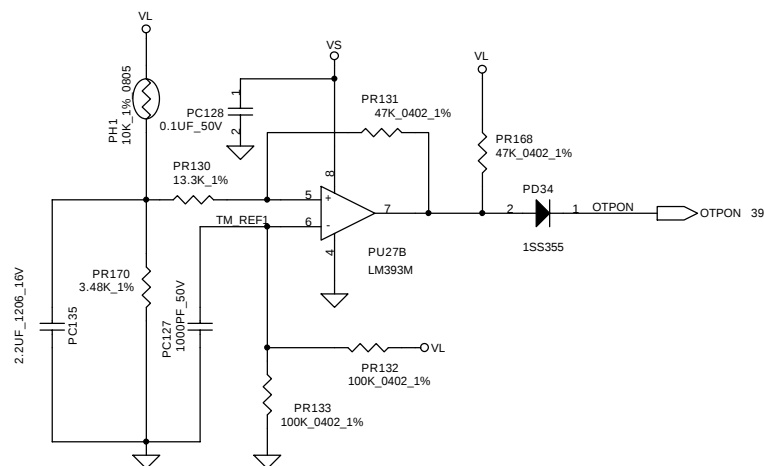
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12 Cells Charger OVP : 18.059V



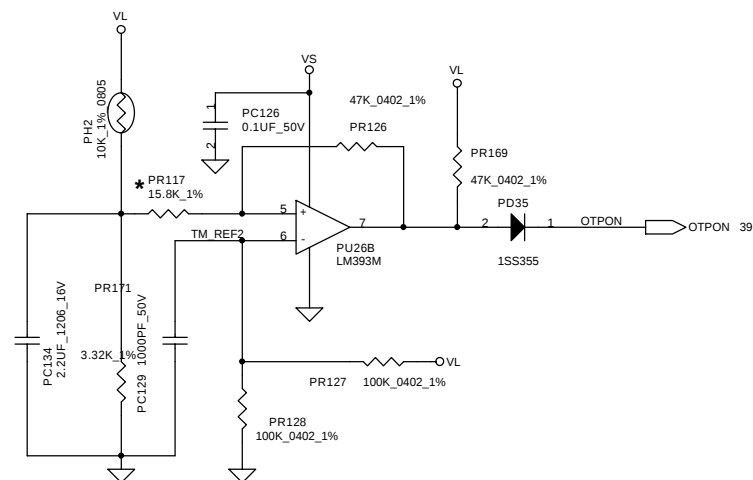
PH1 under CPU botten side :

CPU thermal protection at 76 degree C
Recovery at 45 degree C



PH2 near main Battery CONN :

BAT. thermal protection at 82 degree C
Recovery at 45 degree C



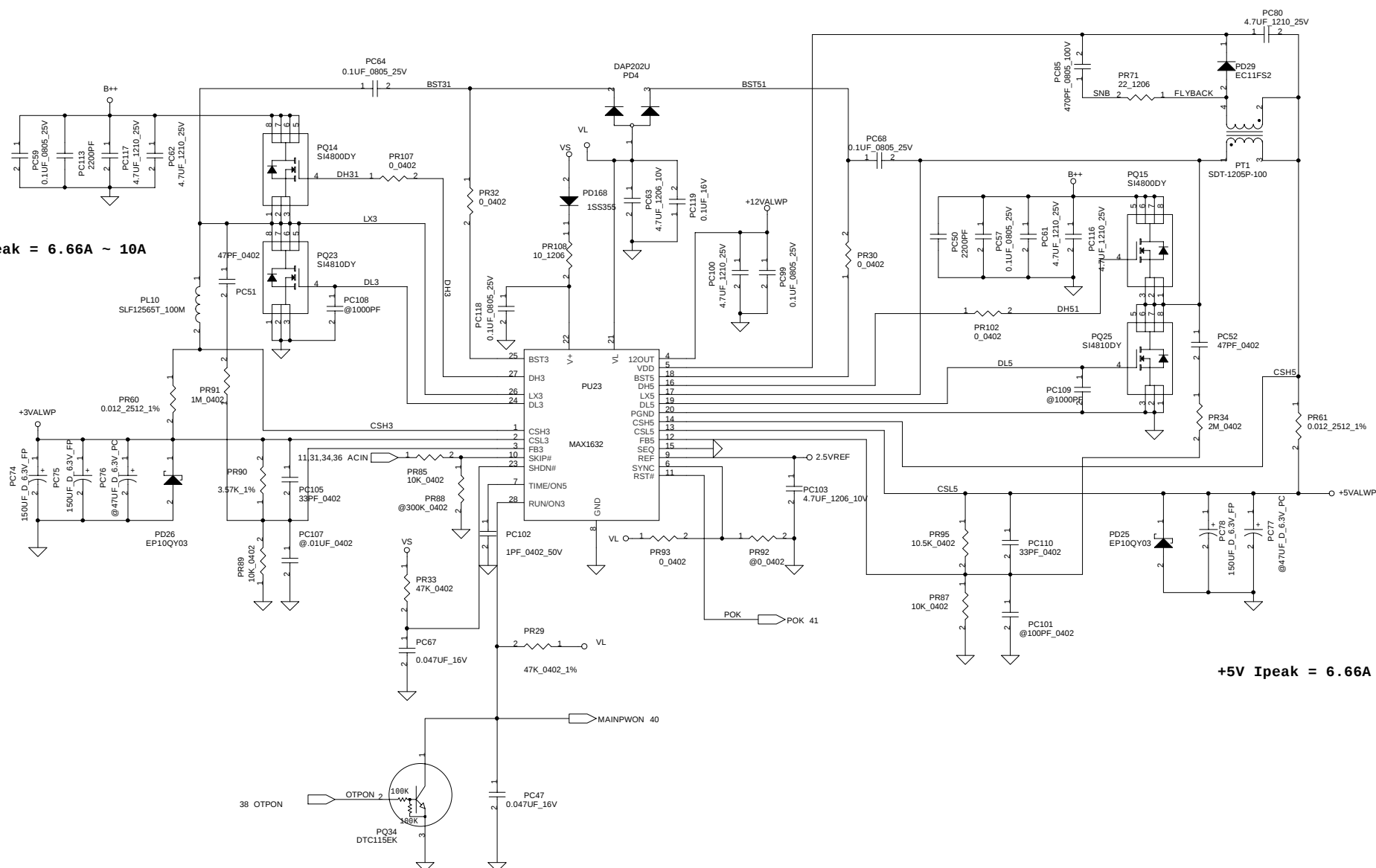
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+3.3V Ipeak = 6.66A ~ 10A

+5V Ipeak = 6.66A ~ 10A



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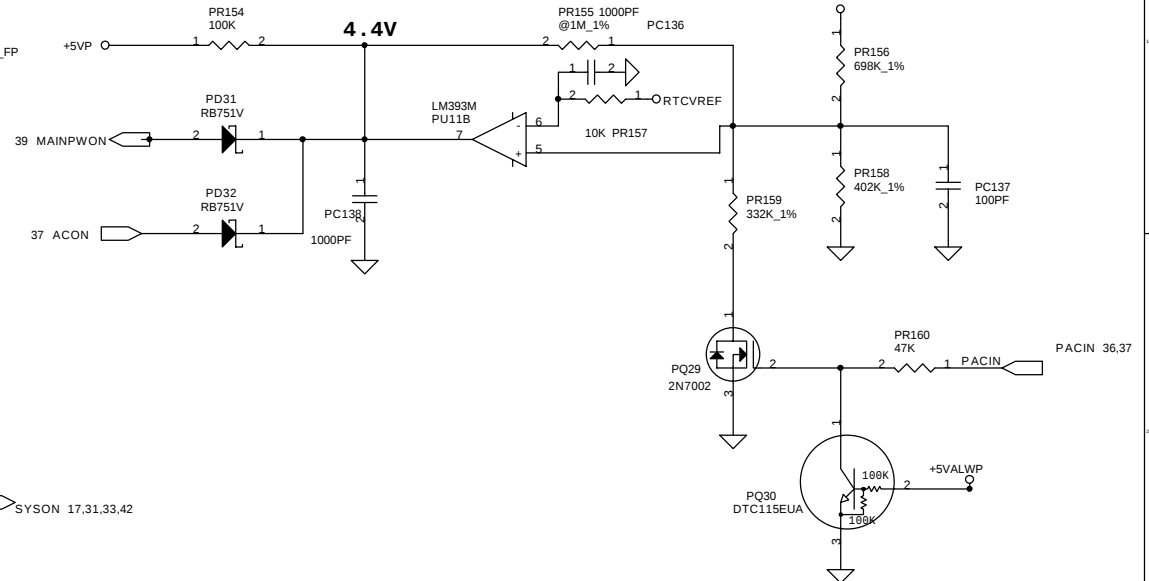
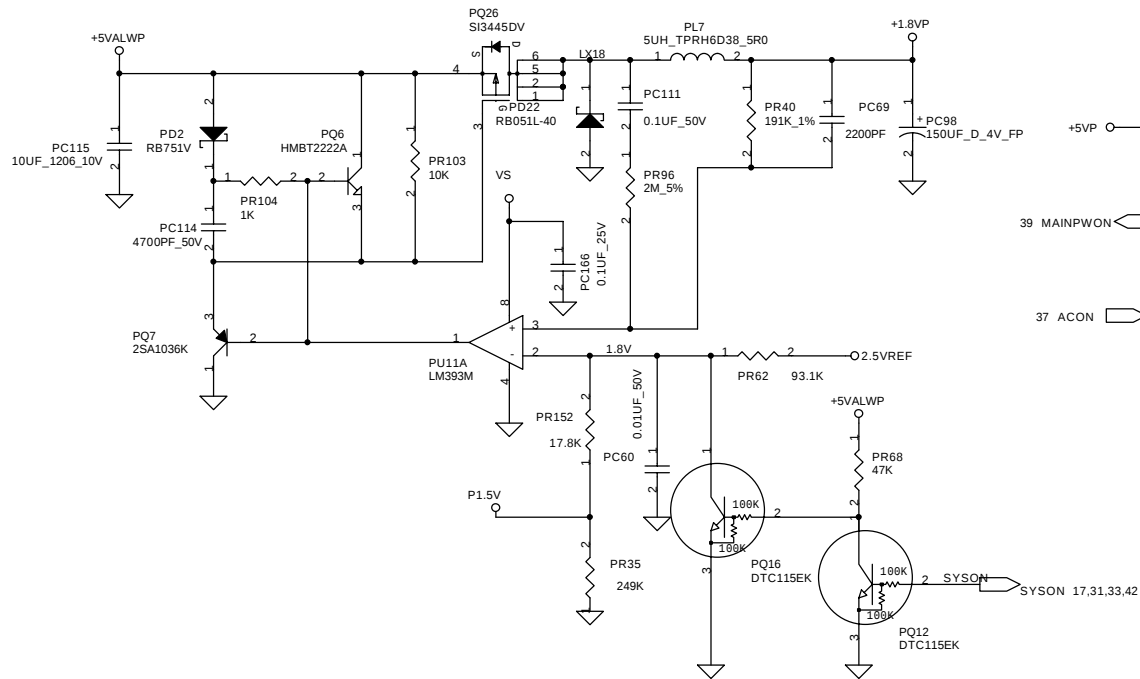
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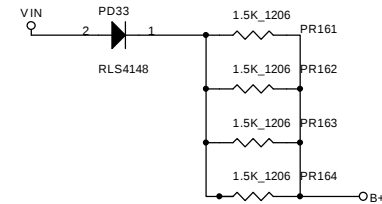
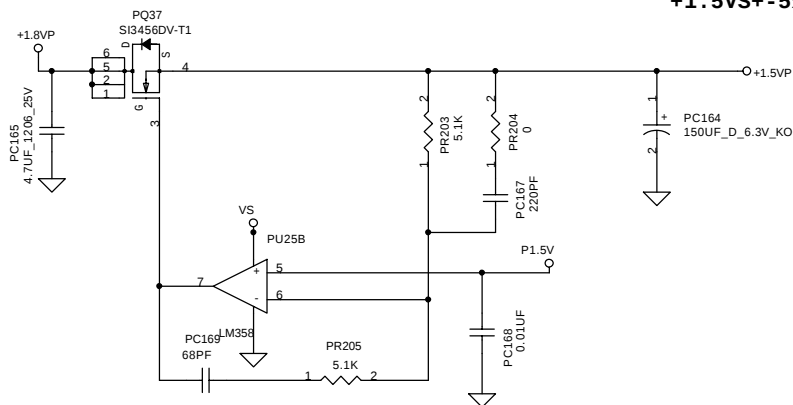
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+1.8VP+-5%

1.8V/1.5V



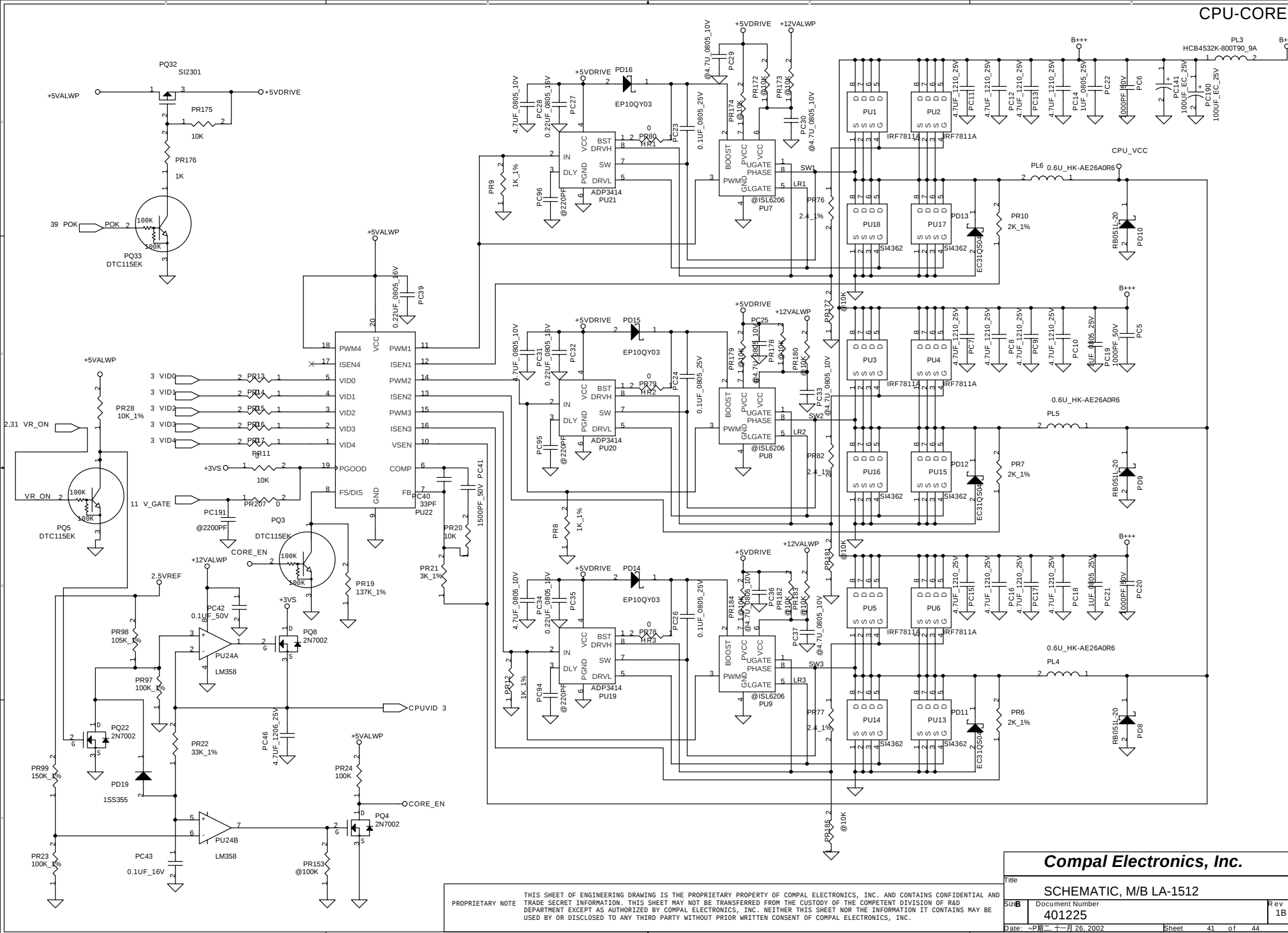
+1.5VS+-5%



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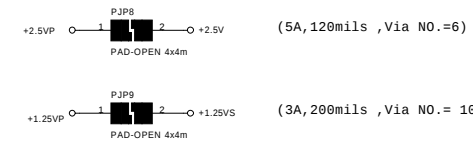
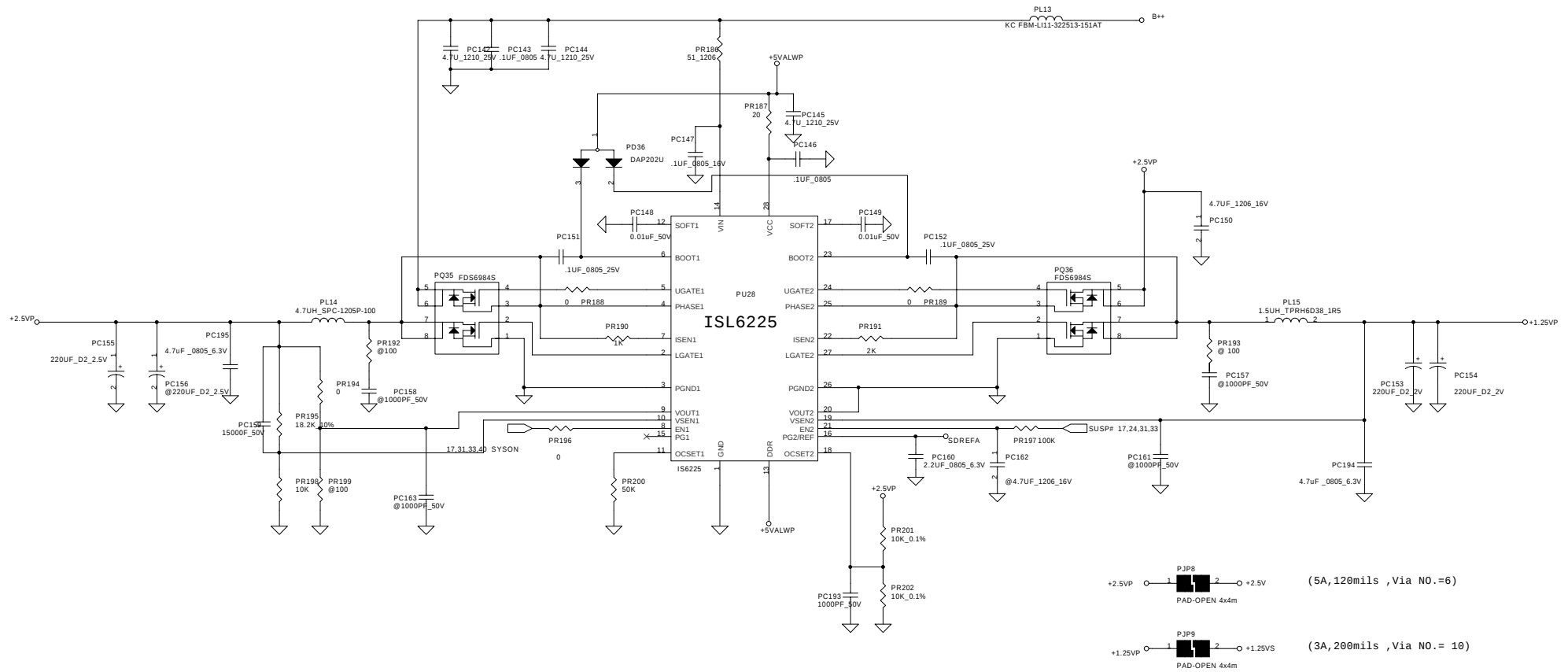
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+2.5V/+1.25V



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ATR11 PIR LIST

HW PIR LIST

PWR PIR LIST

ATEST 9/6~			
1	+2.5V LEVEL ERROR	Remove R111 100ohm Change R108 from 100 to 0 ohm	P15
2	SDRAM BANK select error	Change CLK_SDRAM3 from U22.F3 to U22.E3 Change CLK_SDRAM5 from U22.H5 to U22.G5 Change RRAS#3 from U22.H7 to U22.F7 Change RRAS#5 from U22.G6 to U22.H6	P5
3	Add CPU VCCVID pin	Add R492 connect to U23.AF3	P3
4	TV OUT output error	Change CRMA from U8.AF15 to U8.AF16 Change LUMA from U8.AF16 to U8.AF15	P12
5	Add MK1707 output terminate	Add R494 22 ohm	P12
6	Add low pass filter for MK1707 power pin	Add L47	P12
7	Change VGA option resistor to jumper	Change R284 to J3 Change R286 to J4 Change R295 to J5 Change R296 to J6	P15
8	Reserve +2.5V option control	Add R495 for SYS0N control Add R496 for SUSP control	P15
9	Change LCD connector type	Change JP1	P16
10	Add TI TPS2042 for USB protect cicuit	Add U56(TPS2042)	P25

BTEST			

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